

24th SpaceWire Working Group Meeting

Thursday, 24 September 2015

SpW- and SpFi-related components - Einstein (09:00 - 10:05)

-Conveners: Giorgio Magistrati

time	[id] title	presenter
09:00	[4] SpaceFibre on Microsemi RTG4 FPGA	Mr FERRER, Albert Dr GONZALEZ VILAFRANCA, Alberto Prof. PARKES, Steve
09:35	[10] Independent implementation of the GigaSpace Wire codec on FPGA	Mr VIACHESLAV, Grishin
09:50	[19] Development of a Space Grade CPU	Prof. FUKUDA, Seisuke

SpW- and SpFi-related components: Continued - Einstein (11:00 - 12:00)

-Conveners: Giorgio Magistrati

time	[id] title	presenter
11:00	[8] SpaceWire Matched Impedance Connector	Mr METTENDORFF, Florent Mr ENOUF, Kevin Mr HERMANT, Stephane