

# Digital Programmable Controller: From Concepts to Space Applications

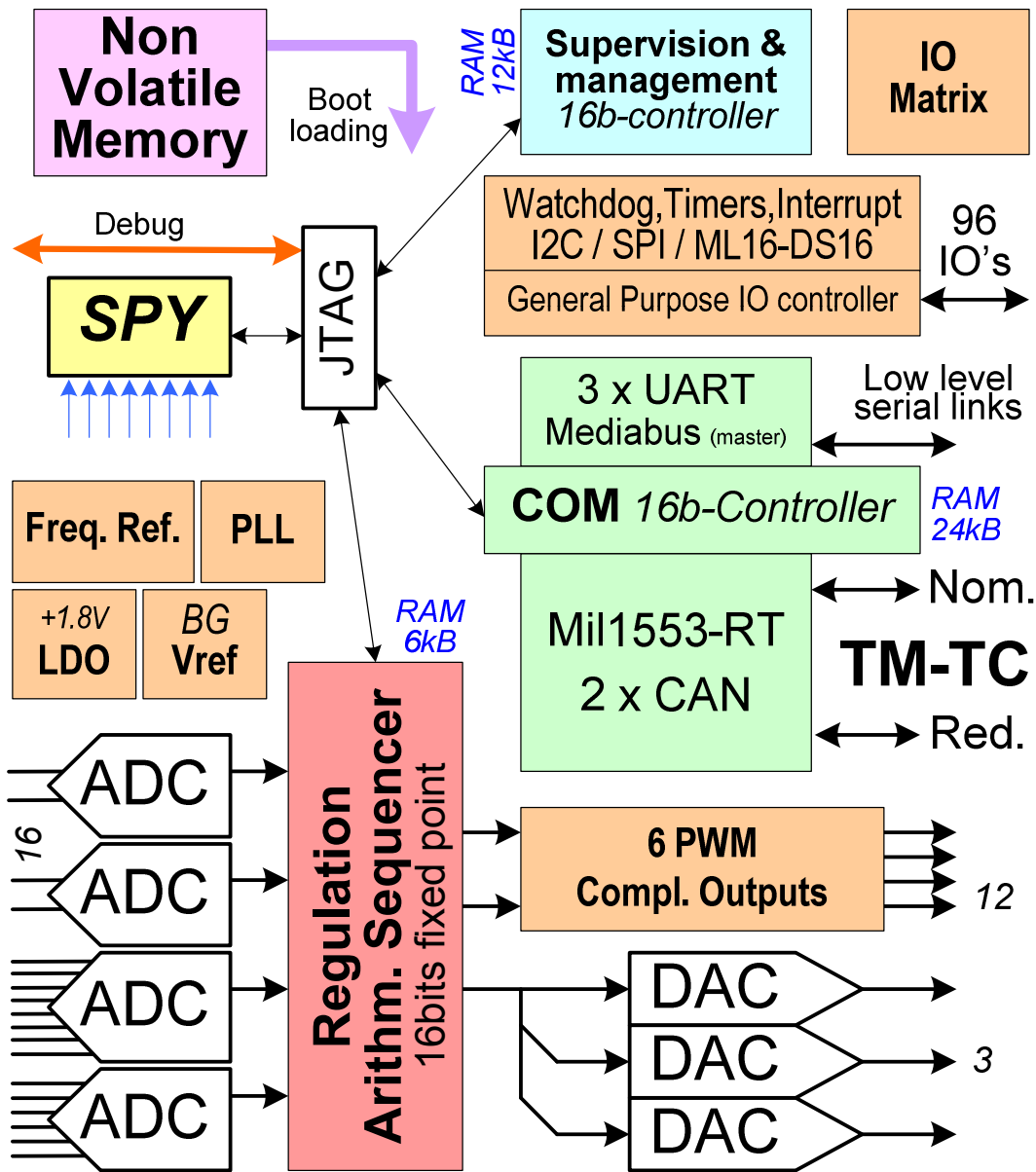
Gothenburg,  
June 13th, 2016



**AMICSA 2016** - Sixth International  
Analogue and Mixed-Signal Integrated  
Circuits for Space Applications



# DPC Overview (reminder)



12/06/2016

## On chip :

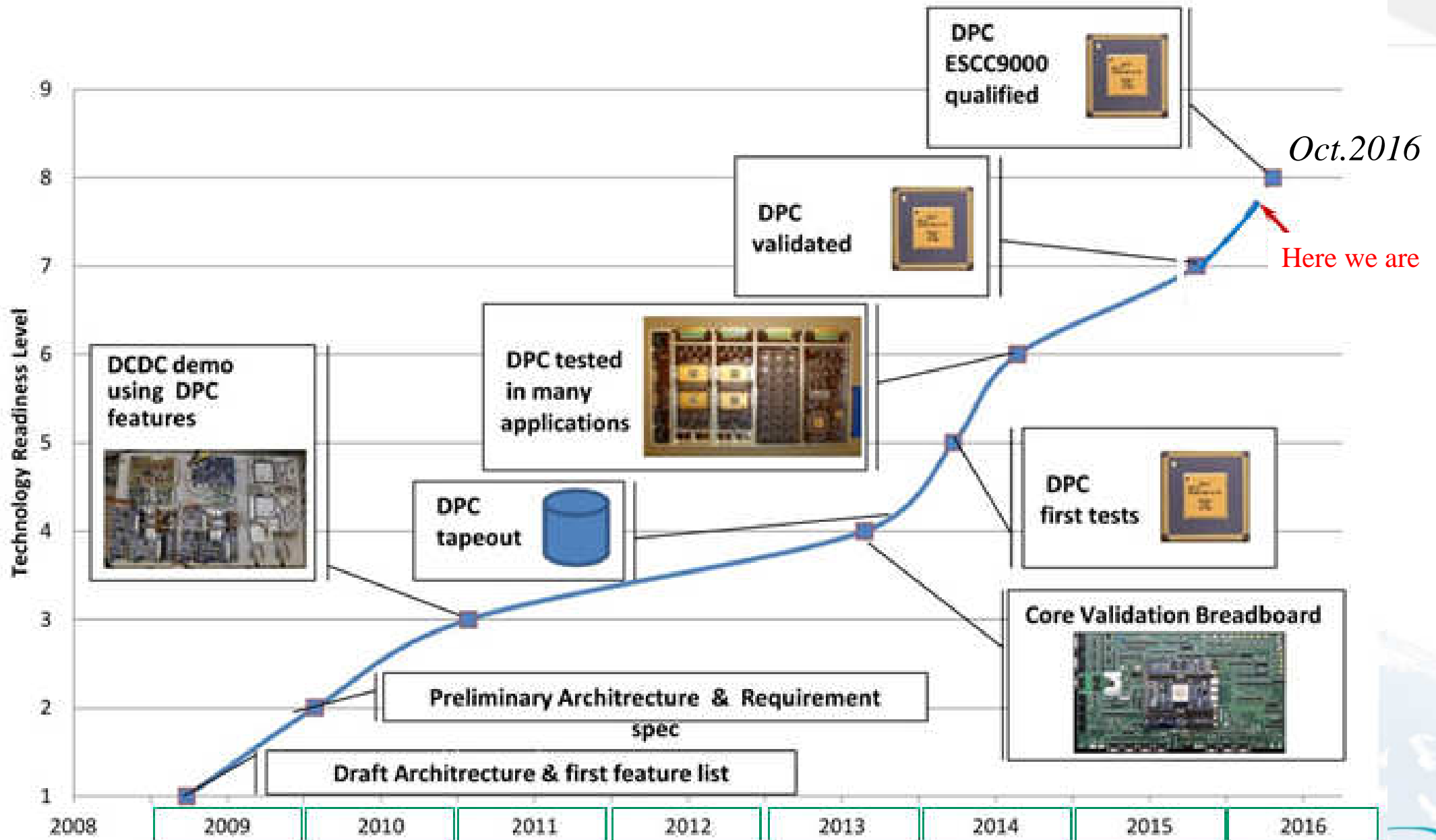
4x CPU 16bits OpenMSP430  
 No OS / One CPU = one task  
 42K memory  
 RC oscillator + 120MHz PLL  
 LDO +1.8V  
 Bandgap  
 13bits ADC + input MUX  
 12 Bits DAC

## Off Chip:

E2prom  
 +3.3V regulator  
 DARE on UMC 0,18μ  
 CQFP 256 pins

OPEN

# TRL evolution vs. time



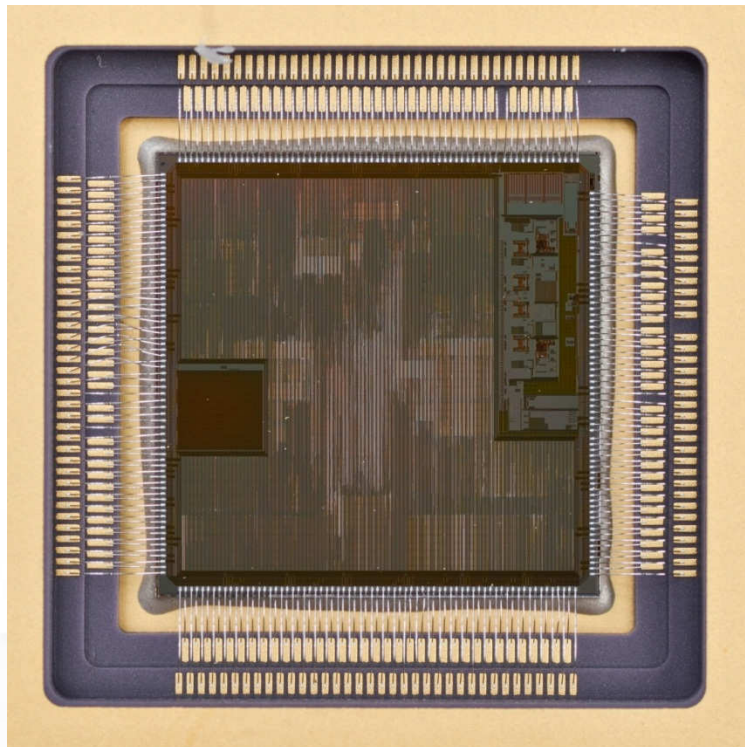
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- DPC enters its latest qualification phase.
  - ESCC9000 completion planned Oct '16.
- First Flight Models are in production now.



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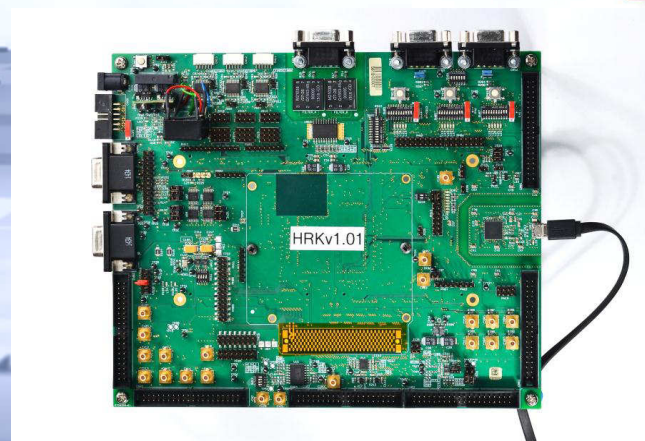
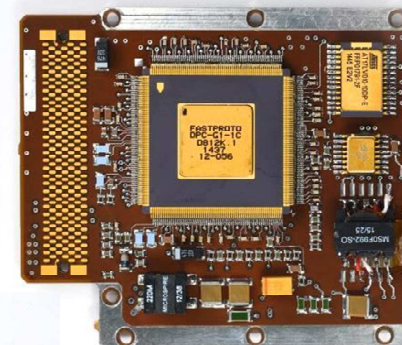
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# Space Applications



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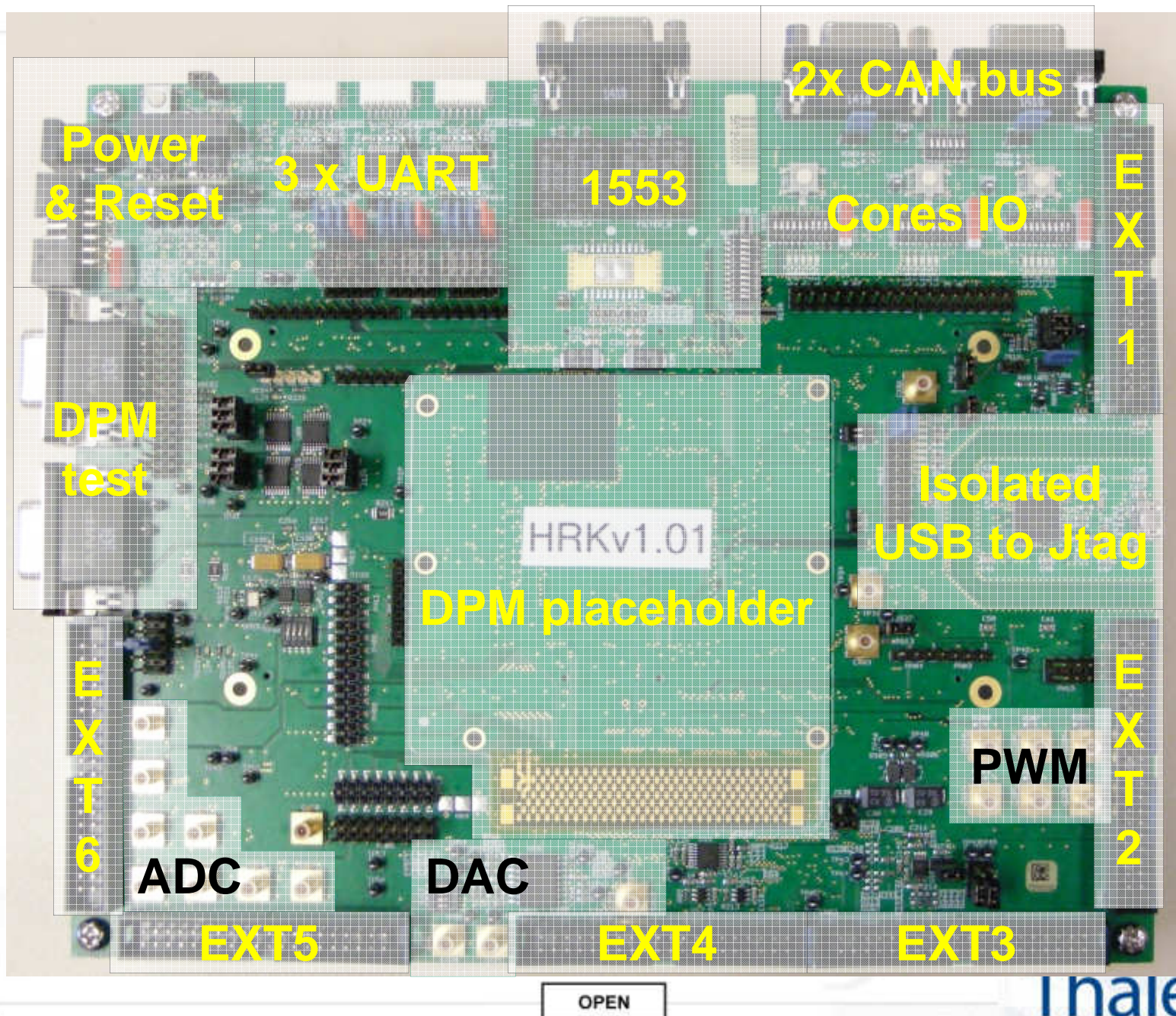
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# DRK board: multiple interfaces



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# Software Toolchain: widely based on Open Source Tools

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Tool Name	Function
<i>misp-gcc</i>	Open source tools for MSP-430, including: <ul style="list-style-type: none"><li>• compiler: msp430-gcc</li><li>• Linker: msp430-ld</li><li>• Object dumper: msp-objdump</li><li>• Debuggers: msp430-gdb</li><li>• Instruction simulator: msp-debug</li><li>• Size analysis: msp430-size</li></ul>
<i>dpc-minidebug</i>	Hardware-oriented graphical interface tool enabling simple interaction with the DPC openMSP430 cores. Allows examining and patching registers and memory, setting breakpoints, halt, run and step by step execution.
<i>dpc-gdbproxy</i>	Provides the proxy function for GDB. Replaces the msp430-gdbproxy provided by the mspgcc toolchain.
<i>dpc-pkt</i>	Transforms .elf file in a format compliant to the packet definition.

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<i>dpc-pkt</i>	Transforms .elf file in a format compliant to the packet definition.
<i>dpc-programmer</i>	NVM programmer tool and global loader. Writes hardware configuration and program packets in the NVM or loads them directly in the DPC and cores memory through the boot manager.
<i>dpc-configuration</i>	Hardware configuration packets editor.
<i>dpc-crc16</i>	Utility to compute and check the CRC on hardware configuration and programming packets.
<i>Mspdebug</i>	Used in DPC as a MSP430 simulator.

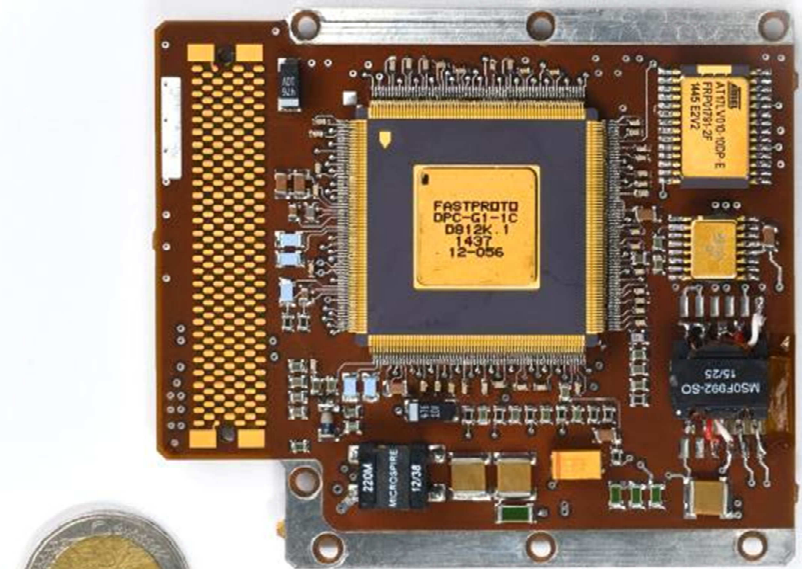
## ➤ Use of existing building blocks

- Reference PBA design or mezzanine
- CAN interface (HW & firmware) ready

- Focus on new applications
- No re-design
- Standard & stable back-plane I/F
- Stable routing of complex function
- Firmware validation at DPM level
- Easier partnerships

## ➤ Available Q4-2016

➤  
DPC+2xCAN+LCL+dc-dc



DPM

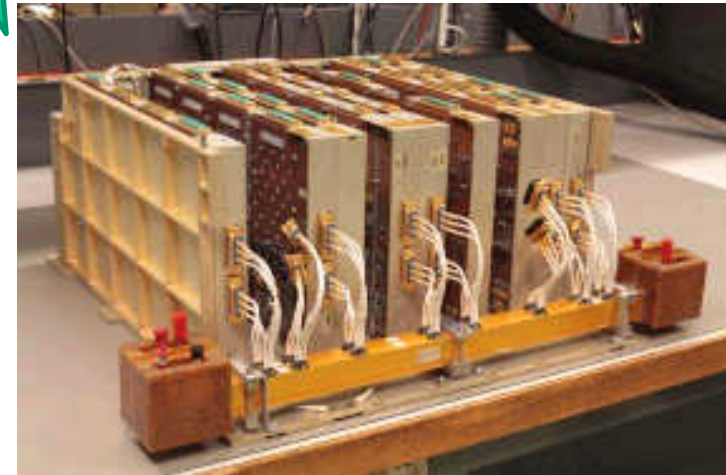


2014

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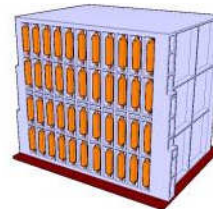
## HIGH POWER AVIONICS

- Modular PLDIU & PFDIU for SB NEO
- Full power distribution and avionics
- CAN backplane bus
- 1 DPC per board

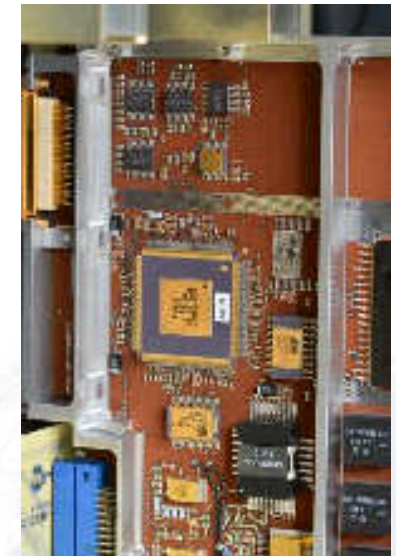


## MEDIUM POWER AVIONICS

- Application programs:
  - Science & observation
  - Exploration



2015



## Miscellaneous equipment

2016



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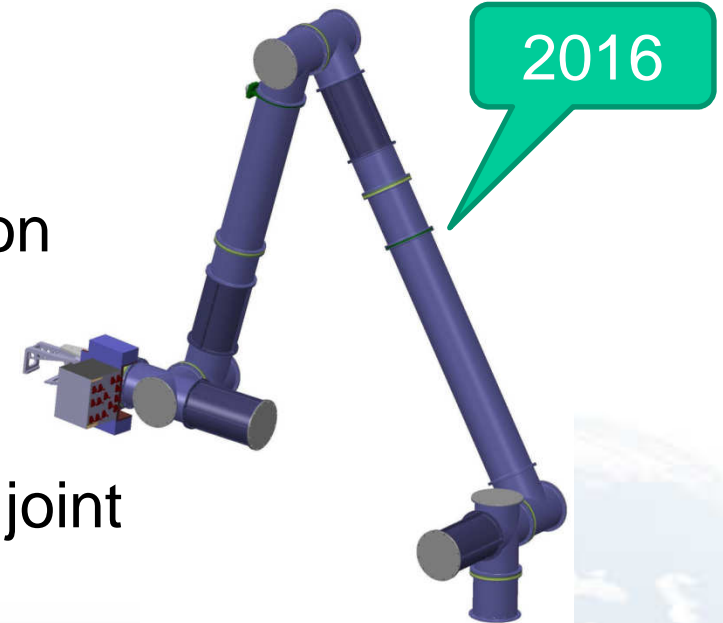
### ONERA: Gyroscopes

- Stimuli, acquisition, processing & reporting

### DLR: robotic arm

#### BLDC Motor Control

- local power supply with latch-up protection
- motor power inverter
- communication and control unit
- joint torque and position sensor for each joint
- motor commutation sensor



# Acknowledgements



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- ESA project team for their great support and many advices in conducting this project : Richard Jansens, Claudio Monteleone, David Sanchez de la Llana, Cesar Boatella, Rok Dittrich, Matthias Gollor, Jesus Rancano, King Lam .... and their colleagues.

- IMEC and ICsense development teams.



- Olivier Girard , who spent hours in the design of the openMSP430 core and has finally posted it in free BSD licence available to anyone's usage on internet
- M. Durvaux for its clever guidance in the selection of this processor, the top-level chip architecture.
- ONERA and specially Dr Jean Guerard for the feedback he provided as “customer using the DPC” that helped us to improve the efficiency of the DPC development tools.
- Mr. Hans Juergen Sedlmayr from DLR for his kind support in publishing this preliminary information on testing the DPC in a robotic context.



## DPC ASIC status

- DPC enters its latest qualification phase. ESCC9000 completion planned Oct '16.
- First Flight Models ASICs are in production now.



## DPC-based applications

- Many DPC-based applications are already in lab validation, not only in Thales.
- External parties have already developed their own application with the DPC

## Availability

- Lab Evaluation boards ( DPC Reference kit DRK) are available now.
- DPC Plugin Modules ( DPM mezzanine) will be available Q4 2016.
- DPC public data sheet available very soon on ESA website

