

AMICSA & DSP 2016

Wednesday, 15 June 2016

Session 1: Rad-Hard DSP Chips (14:45 - 15:45)

-Conveners: Sandi Habinc

time	[id] title	presenter
14:45	[35] Scalable Sensor Data Processor: Architecture and Development Status	Mr PINTO, Ricardo
15:15	[40] RC64: High Performance Rad-Hard Manycore DSP	Prof. GINOSAR, Ran