

AMICSA & DSP 2016

Thursday, 16 June 2016

Session 6: IP Cores, FPGAs, and their Synergies with DSPs (11:30 - 12:30)

-Conveners: Jan Andersson

| time | [id] title | presenter |
|-------|--|---------------------|
| 11:30 | [15] Multi-core DSP sub-system IP | Dr RAUWERDA, Gerard |
| 12:00 | [47] DSP and FPGA: Competition, Synergy, and Future Integration in Space ASICs | Dr TRAUTNER, Roland |