

AMICSA & DSP 2016

Monday, 13 June 2016

Radiation-hardened technologies for analogue and mixed-signal ICs (14:00 - 15:20)

-Conveners: Rok Dittrich

time	[id] title	presenter
14:00	[12] Using a Standard Commercial Process for Full Custom Rad Hard Mixed-Signal Design	Mr LÜCK, Volker
14:20	[22] Radiation-Hardened SiGe BiCMOS Technologies for Analogue and Mixed-Signal ICs	Mr CIRILLO, Maurizio
14:40	[48] Incorporating more in-depth radiation knowledge in the DARE180U analog design kit	Mr VERHAEGEN, Staf
15:00	[30] Development of a Digital Temperature Transducer ASIC in a 28 nm FD-SOI CMOS Process for a Spaceborne Low Power Sensor Bus	Mr RONER, Markus Mr BORA, Pragoti Pran