

AMICSA & DSP 2016

Tuesday, 14 June 2016

Full custom digital, analogue, or mixed-signal: Data Converters (1/2) (09:20 - 10:20)

-Conveners: Rajan Bedi

time	[id] title	presenter
09:20	[8] ESA Cosmic Vision MF ASIC and IPs Development	Mr GONZALEZ, Daniel
09:40	[23] First S-Band capable dual 12bit 1.5GSPS ADC in flip-chip hermetic technology	Mr SAVASTA, Eric
10:00	[55] High Resolution Radiation Hardened DAC in CMOS - SOI Featuring a Return - To - Zero Matrix	Dr PAPADAS, Constantin

Full custom digital, analogue, or mixed-signal: Data Converters (2/2) (10:50 - 11:30)

-Conveners: Volker Lück

time	[id] title	presenter
10:50	[19] Serial I/O ADCs/DACs : The Next Giant Leap in Mixed-Signal for Space.	Dr BEDI, Rajan
11:10	[3] Comparison Study of Bulk and SOI CMOS Technologies based Rad-hard ADC in Space	Mr LIU, Hainan