AMICSA & DSP 2016

Wednesday, 15 June 2016

Evaluation and qualification of full custom ICs for space applications (09:20 - 10:00)

-Conveners: Florence Malou

time	[id] title	presenter
09:20	[33] Design Methodology for mixed signal ASIC with prequalified Analog IPs for space applications	Mr BANCELIN, Bernard
09:40	[14] Approval Process of an ESCC Qualified ASIC Supply Chain based on a Mixed-Signal IP Library	Mr STEINKAMP, Jan