

AMICSA & DSP 2016

Thursday, 16 June 2016

Round Table Discussion - DSP and FPGA (12:30 - 12:55)

-Conveners: Roland Trautner

time	[id] title	presenter
12:30	[67] Open discussion on synergies of DSP and FPGA IP, related user requirements, and industrial views	Dr TRAUTNER, Roland