SEFUW: SpacE FPGA Users Workshop, 3rd Edition

Tuesday, 15 March 2016

Fault Tolerance Methodologies and Tools - Newton 1 and 2 (14:45 - 16:00)

   -**Conveners: David Dangla**

time	[id] title	presenter
14:45	[75] Configuration Scrubbing and Mitigation Approaches for the Zynq System-on-Chip	WIRTHLIN, Mike
15:10	[48] SET effects analysis and mitigation on Flash-based FPGAs	Prof. STERPONE, Luca
15:35	[51] FT-UNSHADES2: the User Friendly Framework as an interface for designer support	Dr HIPÓLITO, Guzmán-Miranda

Wednesday, 16 March 2016

<u>Fault Tolerance Methodologies and Tools</u> - Newton 1 and 2 (12:00 - 12:35)

   -Conveners: Agustin Fernandez-Leon

time	[id] title	presenter
12:00	[34] The Benefits of Feedback TMR for SEU Tolerance of SRAM FPGA Designs	WIRTHLIN, Mike

<u>Fault Tolerance Methodologies and Tools</u> - Newton 1 and 2 (16:35 - 17:25)

   -Conveners: Florent Manni

time	[id] title	presenter
16:35	[43] OLT(RE)²: A Tool Flow for Mitigation of Permanent Faults in Reconfigurable Systems	Mr COZZI, Dario Mr KORF, Sebastian
17:00	[46] Using Dynamic Circuit Specialisation to Enable Microreconfigurations for Space Applications	Mr KULKARNI, Amit

Thursday, 17 March 2016

<u>**Fault Tolerance Methodologies and Tools**</u> **- Newton 1 and 2 (15:15 - 16:05)**

  -Conveners: David Merodio Codinachs

time	[id] title	presenter
15:15	[47] Enhancing the Reliability of COTS SRAM-based FPGAs with Microreconfiguration for SEU Mitigation in Space Applications	Ms KOURFALI, Alexandra
15:40	[49] Highly Reliable System-on-Chip using Dynamically Reconfigurable FPGAs	Prof. STERPONE, Luca