SEFUW: SpacE FPGA Users Workshop, 3rd Edition

Tuesday, 15 March 2016

Industrial Experiences - Newton 1 and 2 (12:35 - 13:00)

-Conveners: David Merodio Codinachs	5
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time [id] title	presenter
12:35 [40] Feedback on Xilinx Virtex-5QV FPGA	Mr SEYCHAL, Florian

Wednesday, 16 March 2016

Industrial Experiences - Newton 1 and 2 (14:00 - 15:15)

-Conveners: Jelle Poupaert

time [id] title	presenter
14:00 [53] Experience gained in Flash-based FPGA for InSight	Mr HUMBERT, Stephane
14:25 [61] Spartan 6 Evaluation for Space Application	PIKE, Tim
14:50 [62] High Performance COTS based Computer with FPGA's implementation	Mr NOTEBAERT, Olivier