

Debug Monitor Software Tool Final Presentation

C. Monteleone

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Development of a Debug Monitor software tool to support the debug of embedded software applications on many cores System on Chip

- The main objective was the development of features unique to a debug monitor tool for visualizing, monitoring and debugging complex System on Chip with many functional units and one or multiple CPUs.
- This development was mainly targeted at SPARC V8 LEON processors based SOCs.
- Initial support for ARM processors was included.
- A contract extension was then applied to support ESA's new AGGA4 navigation chip.
- Activity started in January 2015 and was performed by O.C.E. Technology (Ireland) under GSTP 6.2 (total cost 517K€)