

Multi-threaded processor for space applications - Final Presentation

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Demonstration of multi-threaded technology for space applications

- Follow-up to ITI Type A activity, where initial feasibility of the technology was shown through simulation on synthetic applications
- Prototyping in hardware (FPGA) of a single multi-threaded core
- Demonstration through simulation of multi-core multi-threaded system on a real space application

- Consortium composed of Techne LTD (prime) and Airbus (subcontractor) and Daiteq (subcontractor)
- Contract amount 150K€ under ITI Type B scheme
- Initial duration 14 months
 - Actual contract duration 20 months