

# Reusing Specification to favour product lines

On behalf of the SAVOIR Advisory Group  
Jean-Loup TERRAILLON – ESTEC/TEC-S  
Lead Software Systems Engineer





# Space Avionics Open Interface aRchitecture.



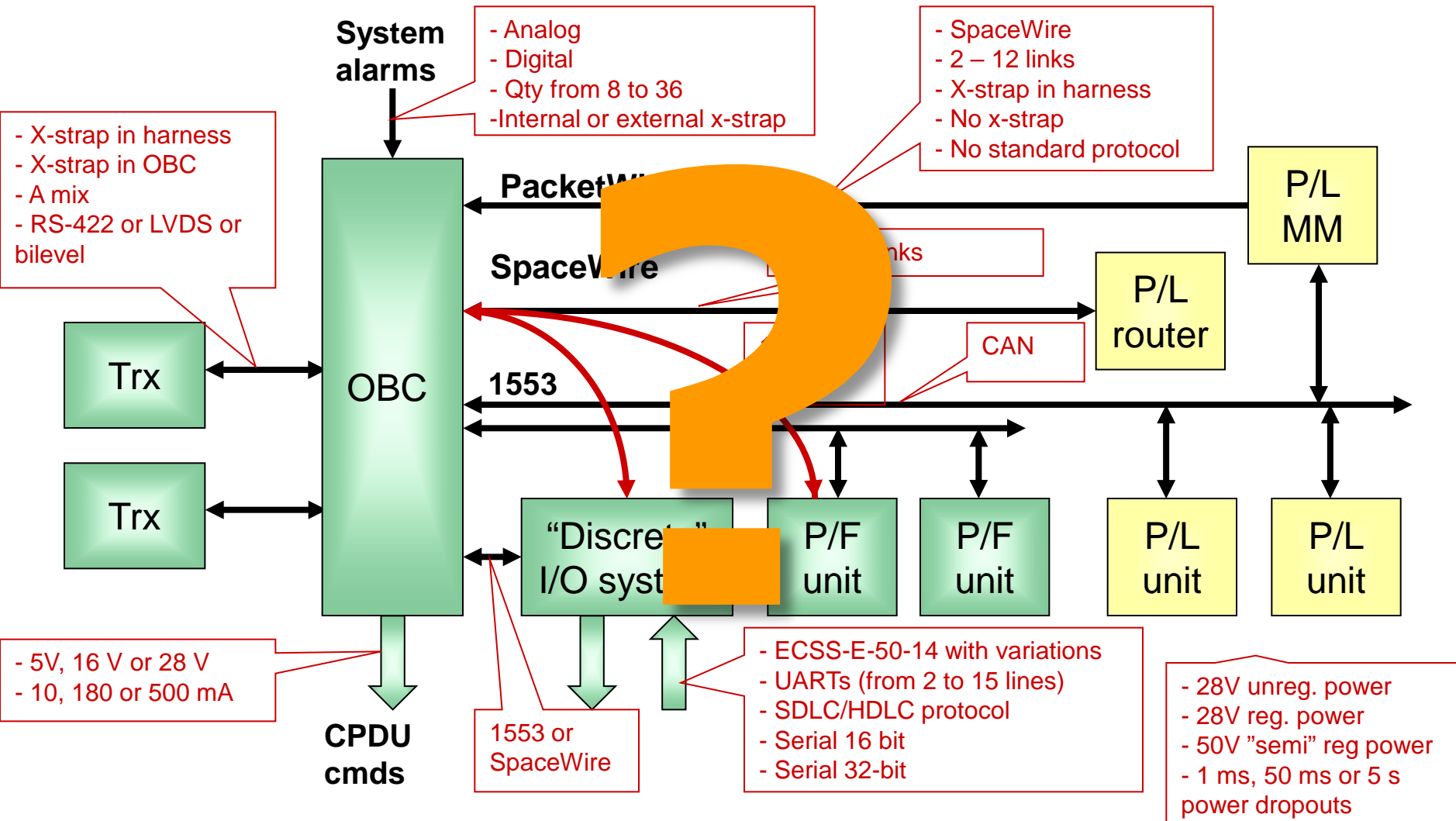
# SAVOIR: Motivation

*“Industry recommends to develop modular architectures fulfilling various mission needs and using a limited number of building blocks or equipments units with limited and robust standards (1553, SpaceWire...) and protocols...”*

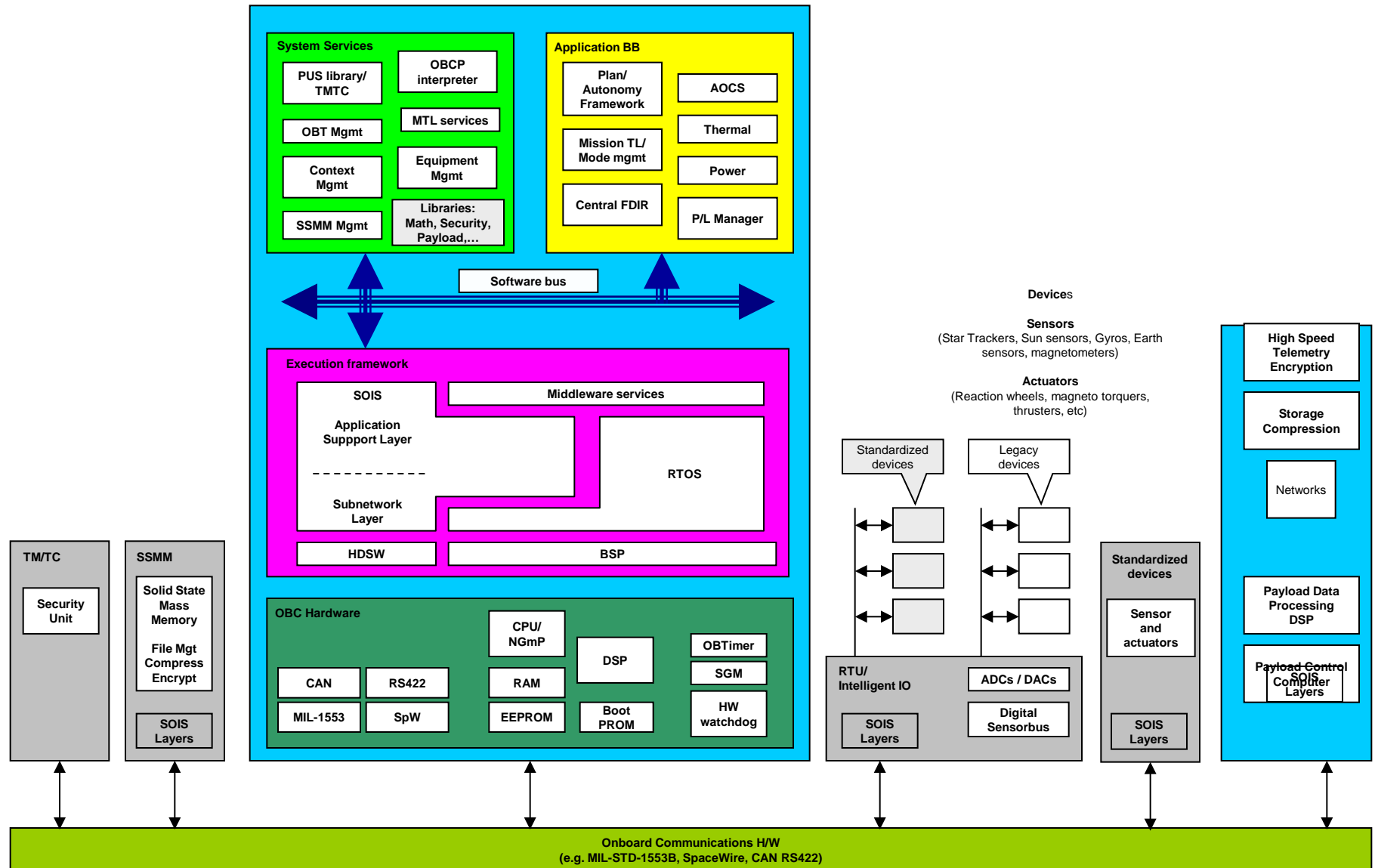
*“**Reference architectures** should be defined mainly in order to identify generic building blocks interfaces...”*

*Eurospace recommendation  
European Harmonisation on  
Avionics Embedded Systems,  
**December 2006***

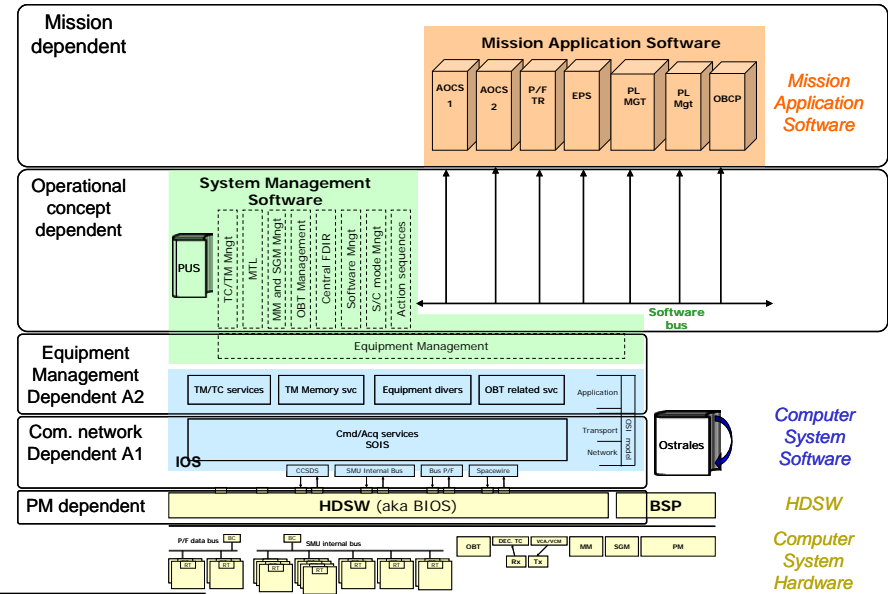
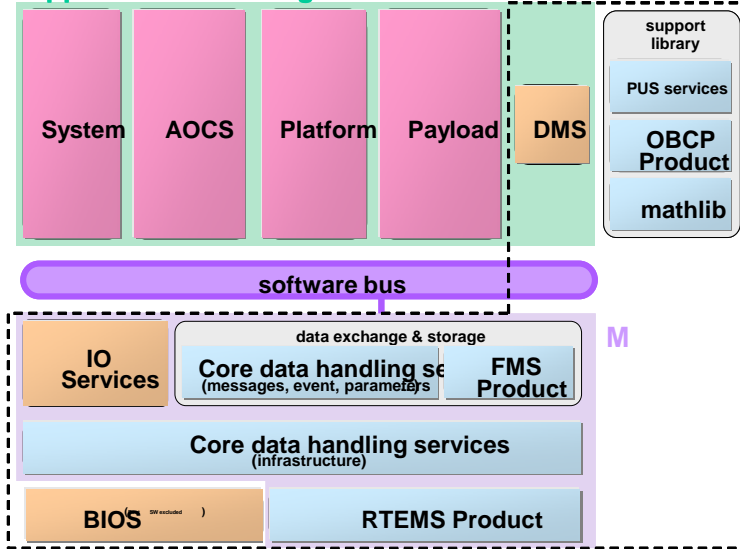
# Avionics architecture Some of the variabilities



# Conceptual Reference Architecture and Building Blocks



## Applications Building Blocks

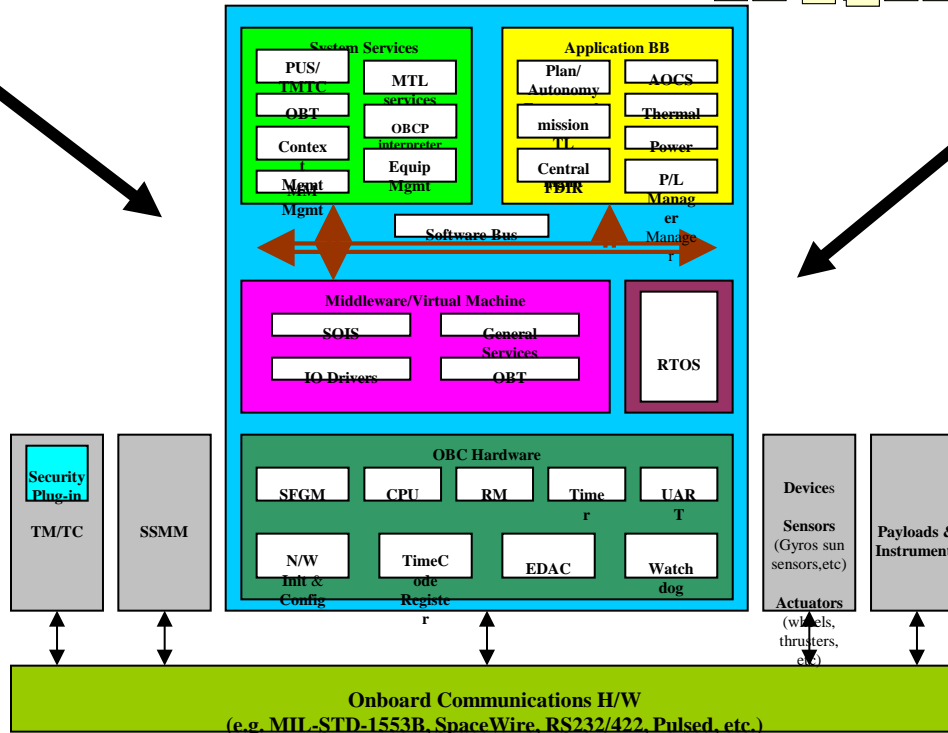


Astrium

TAS

Comms  
Architecture

HW  
Architecture



# SAVOIR Advisory Group: FEDERATE

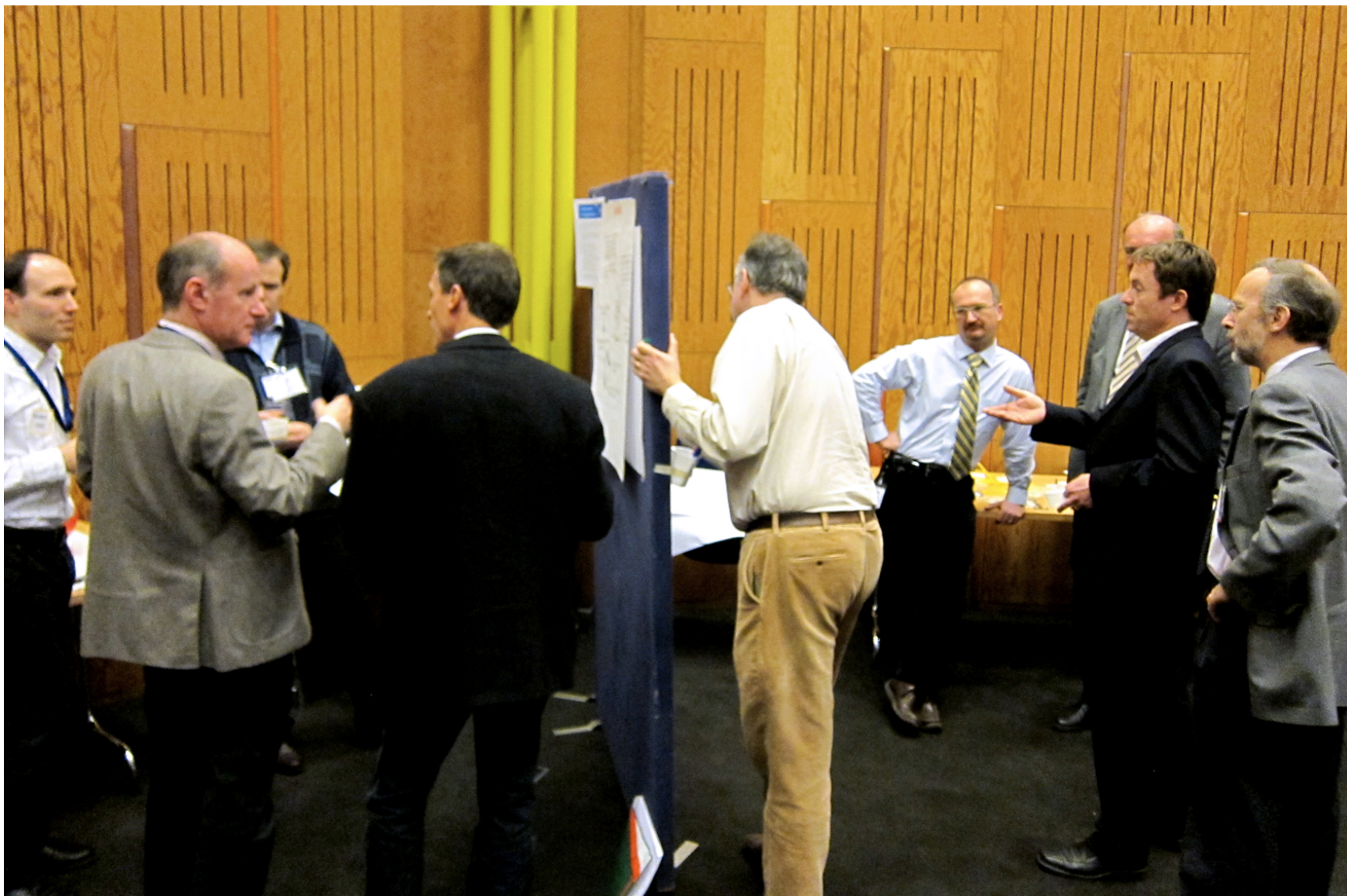






SAVOIR Round Table, ADCSS08,  
ESTEC 29 October 2008





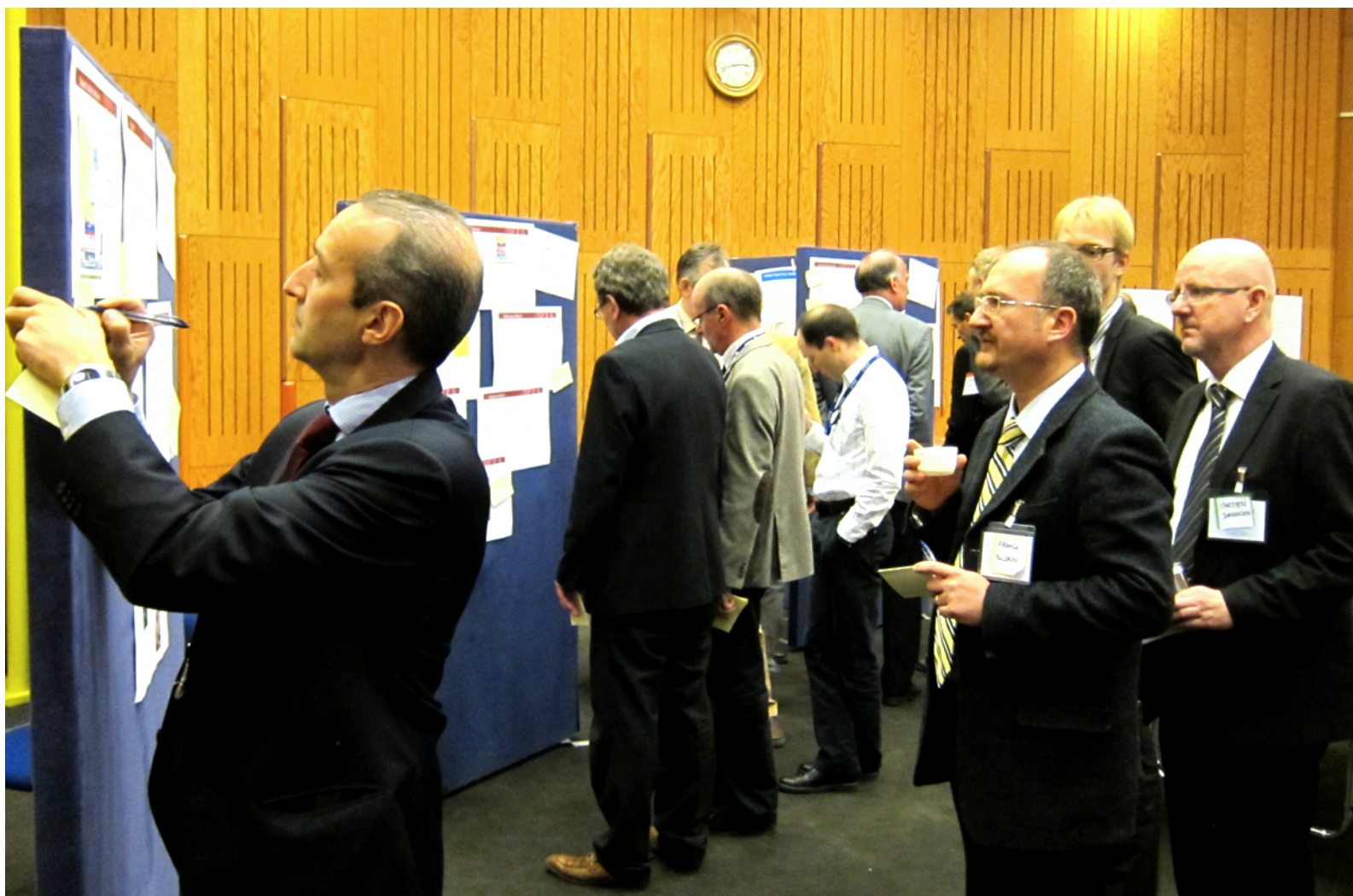
SAVOIR Round Table, ADCSS08,  
ESTEC 29 October 2008





SAVOIR Round Table, ADCSS08,  
ESTEC 29 October 2008





SAVOIR Round Table, ADCSS08,  
ESTEC 29 October 2008



SAVOIR Round Table, ADCSS08,  
ESTEC 29 October 2008





SAVOIR Round Table, ADCSS08,  
ESTEC 29 October 2008

# The avionics reference architecture (HW + SW)



Application software

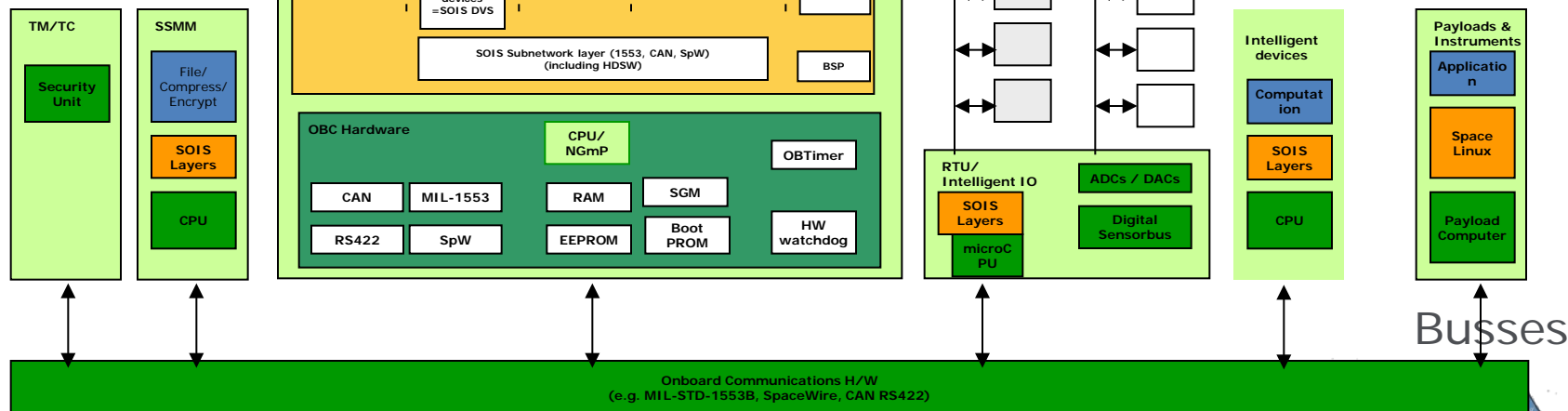
SW bus

Execution platform

computer

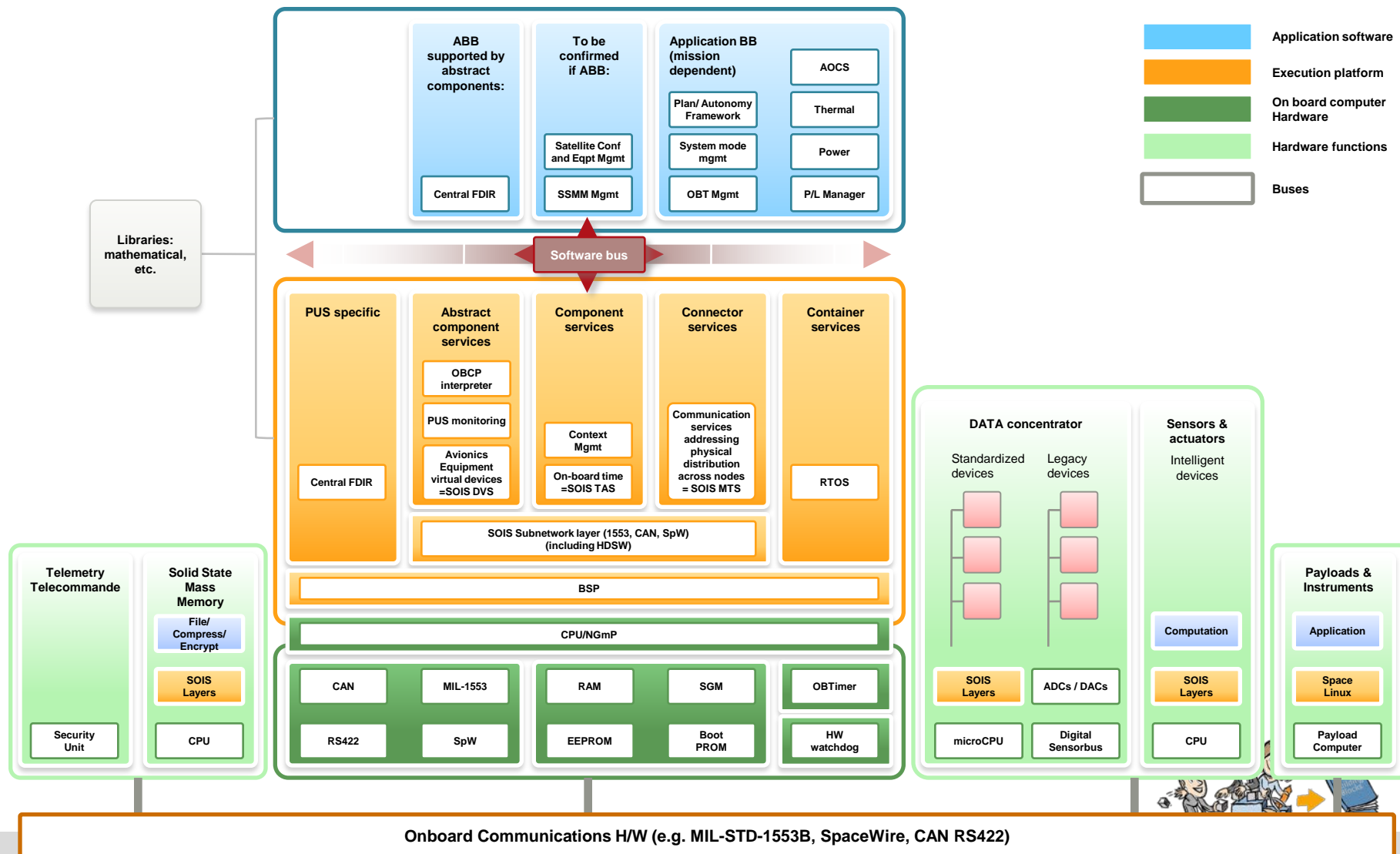
Solid State Mass Memory

Sensors, actuators, payloads, memory

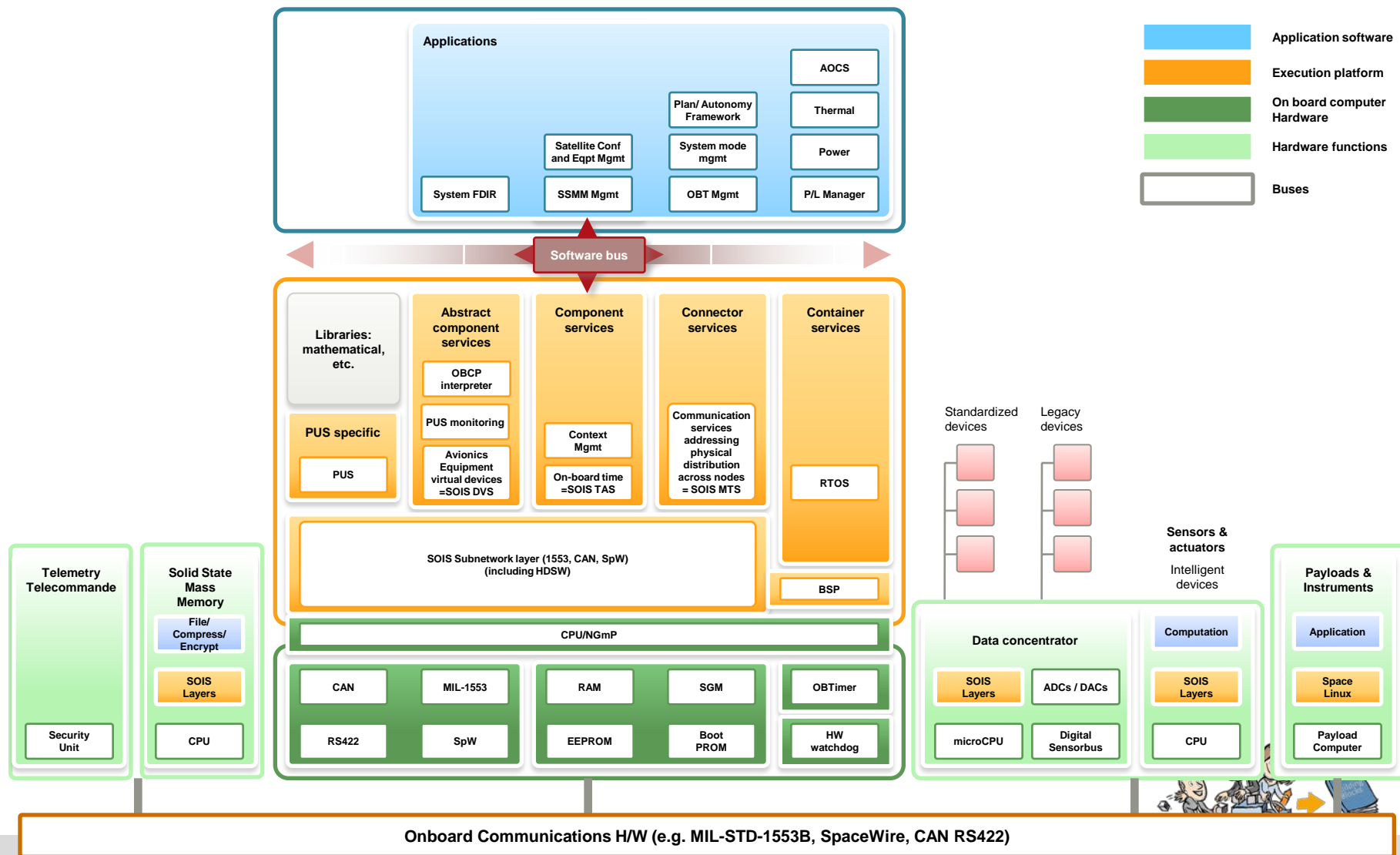




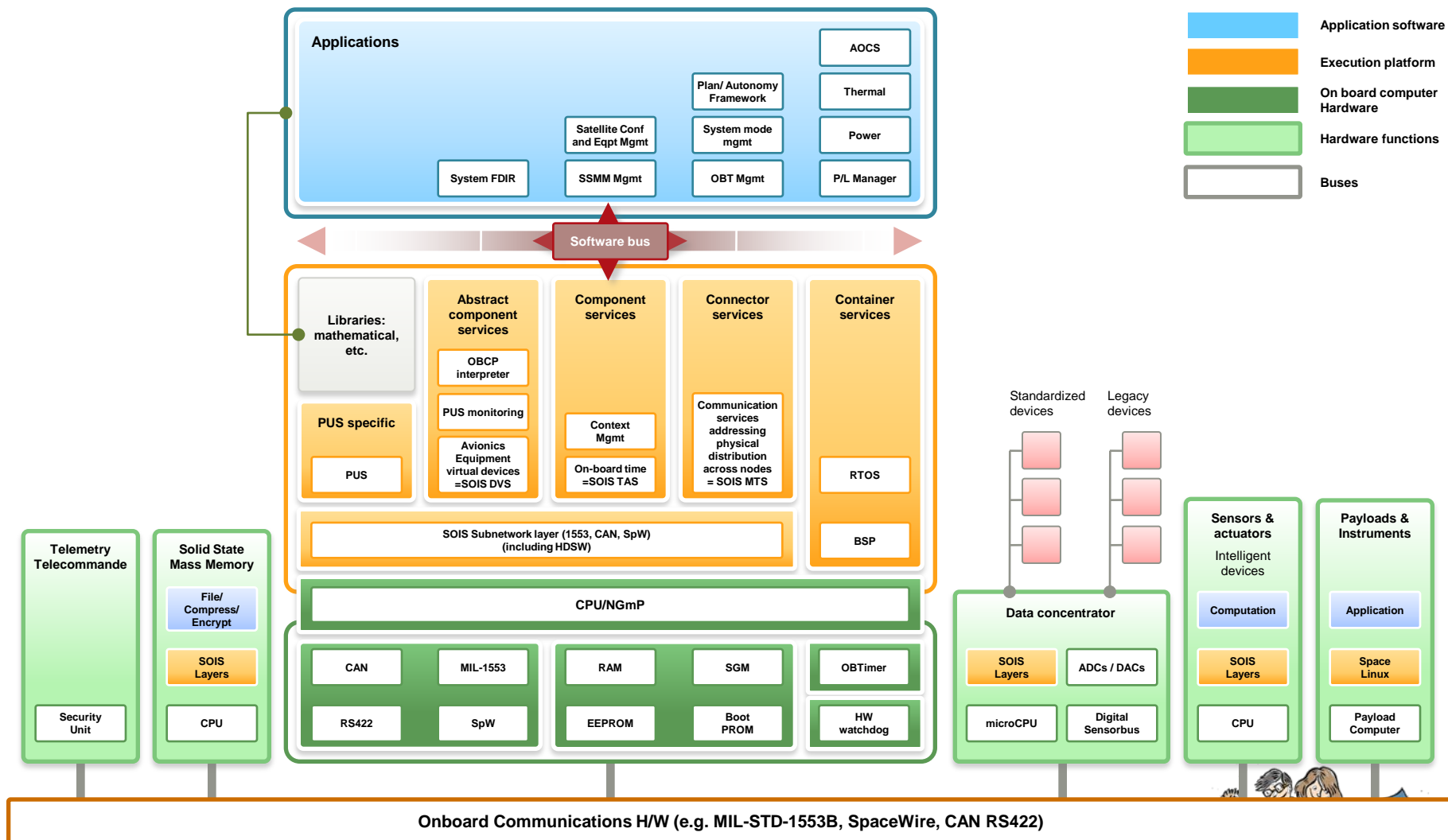
# The avionics reference architecture (HW + SW)



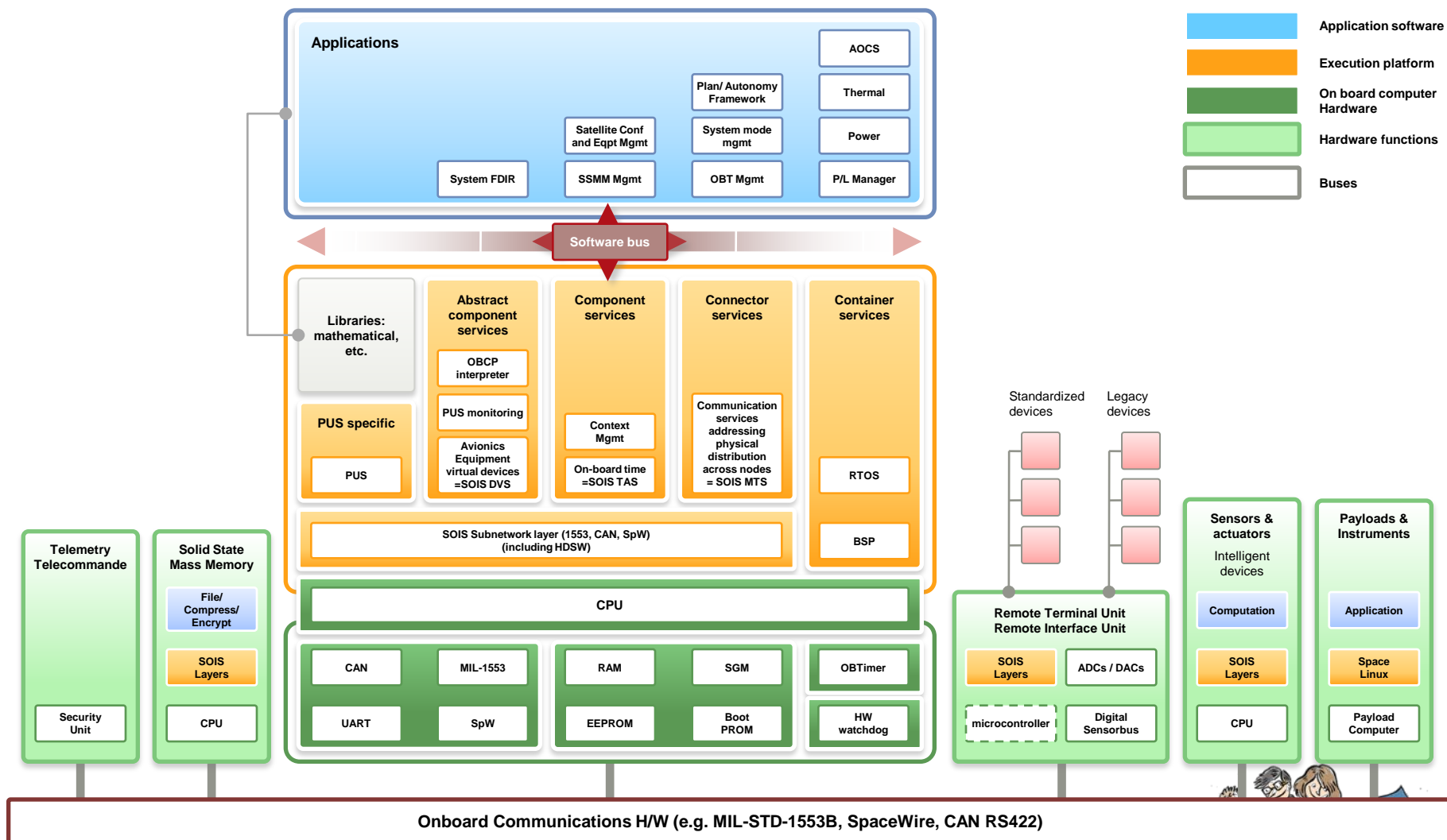
# The avionics reference architecture (HW + SW)



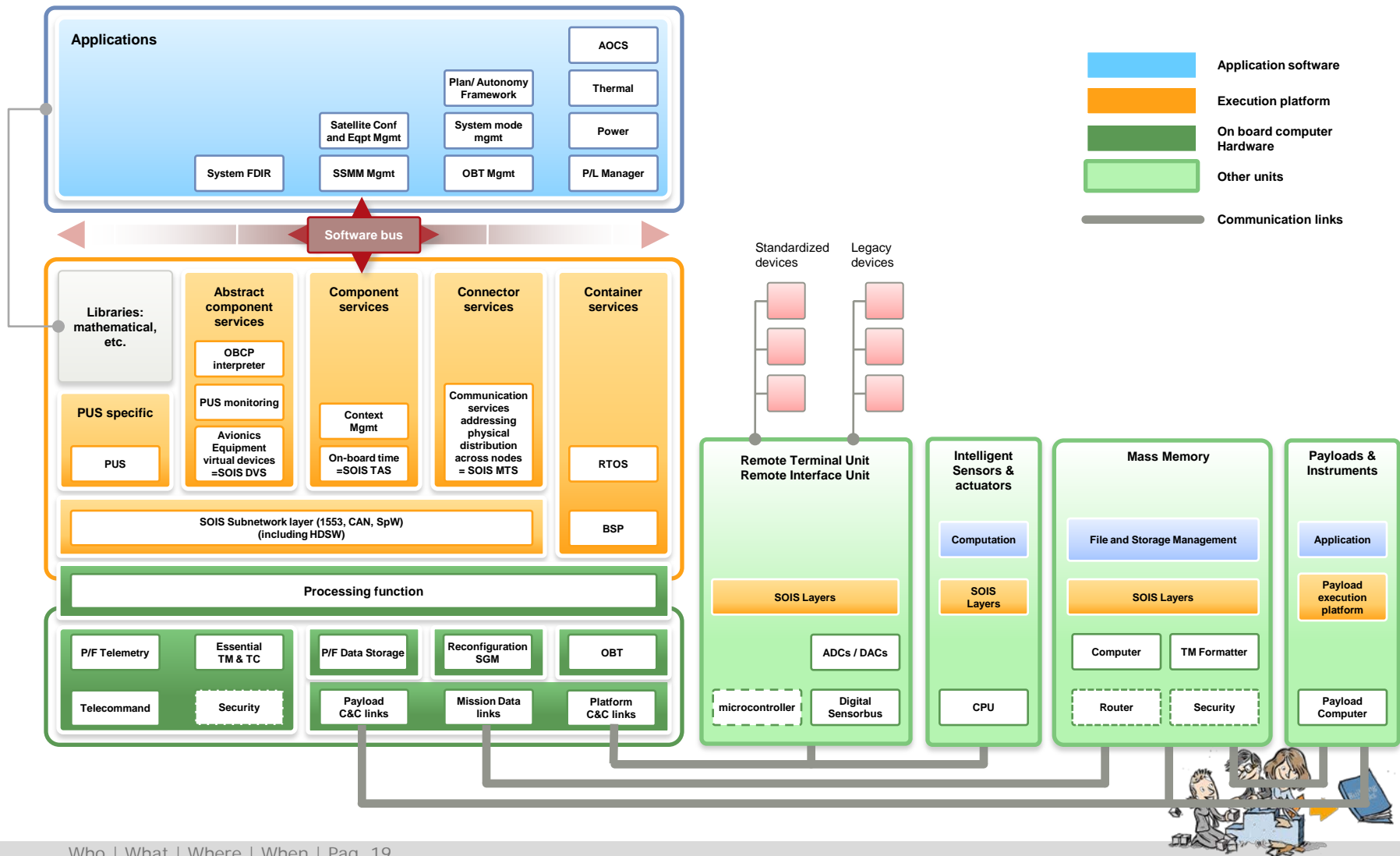
# The avionics reference architecture (HW + SW)



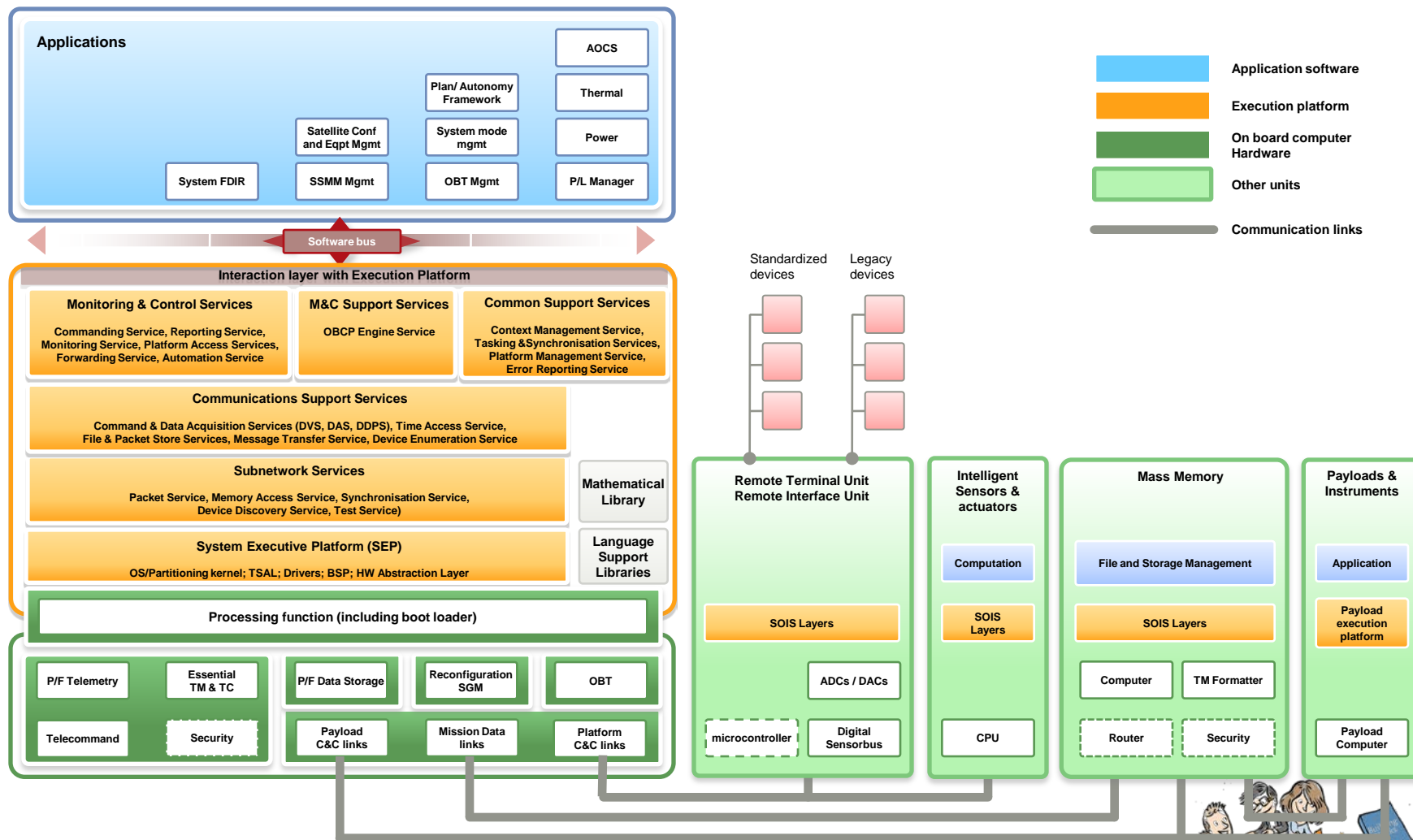
# The avionics reference architecture (HW + SW)



# The avionics reference architecture (HW + SW)

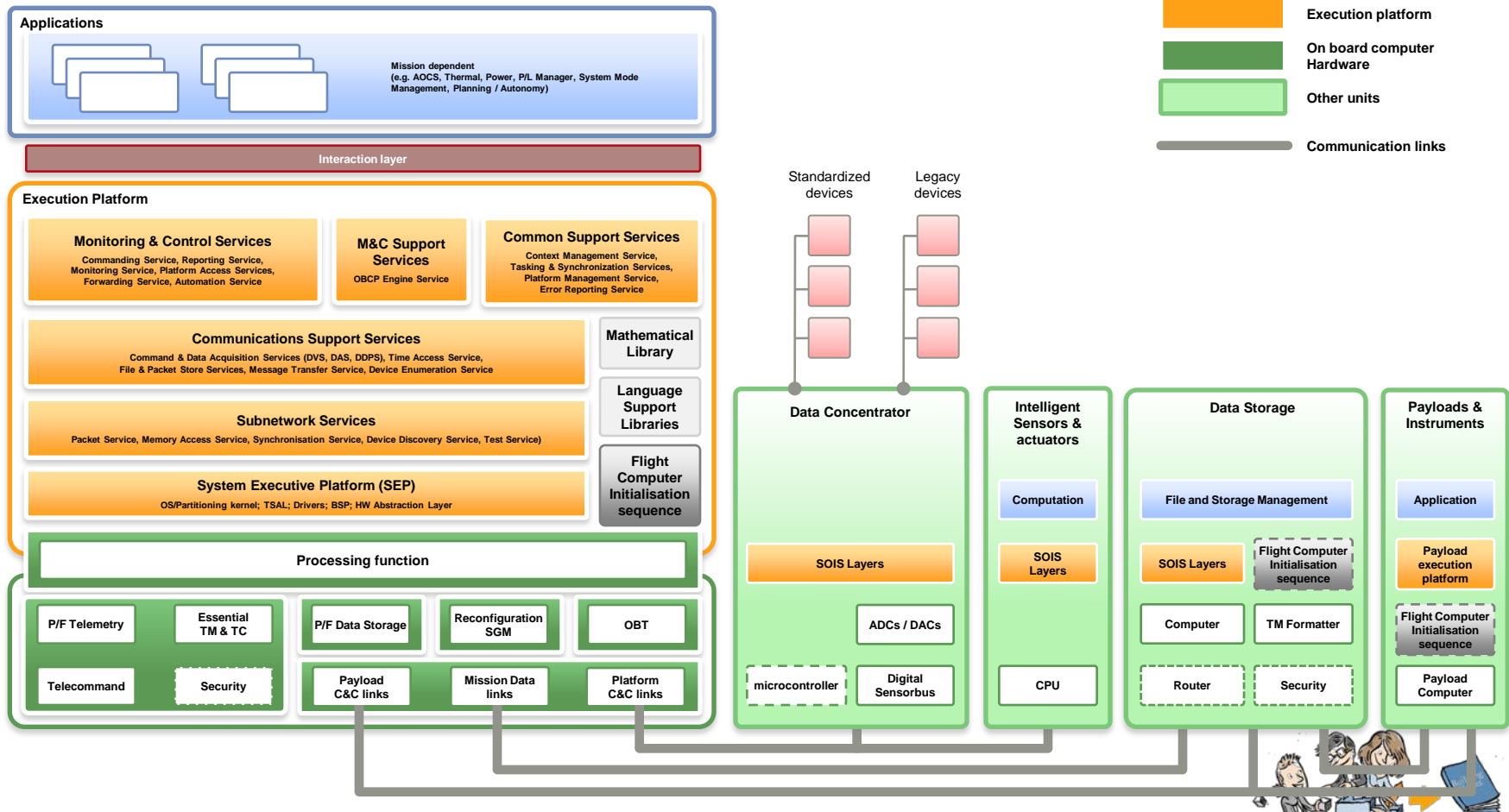


# The avionics reference architecture (HW + SW)

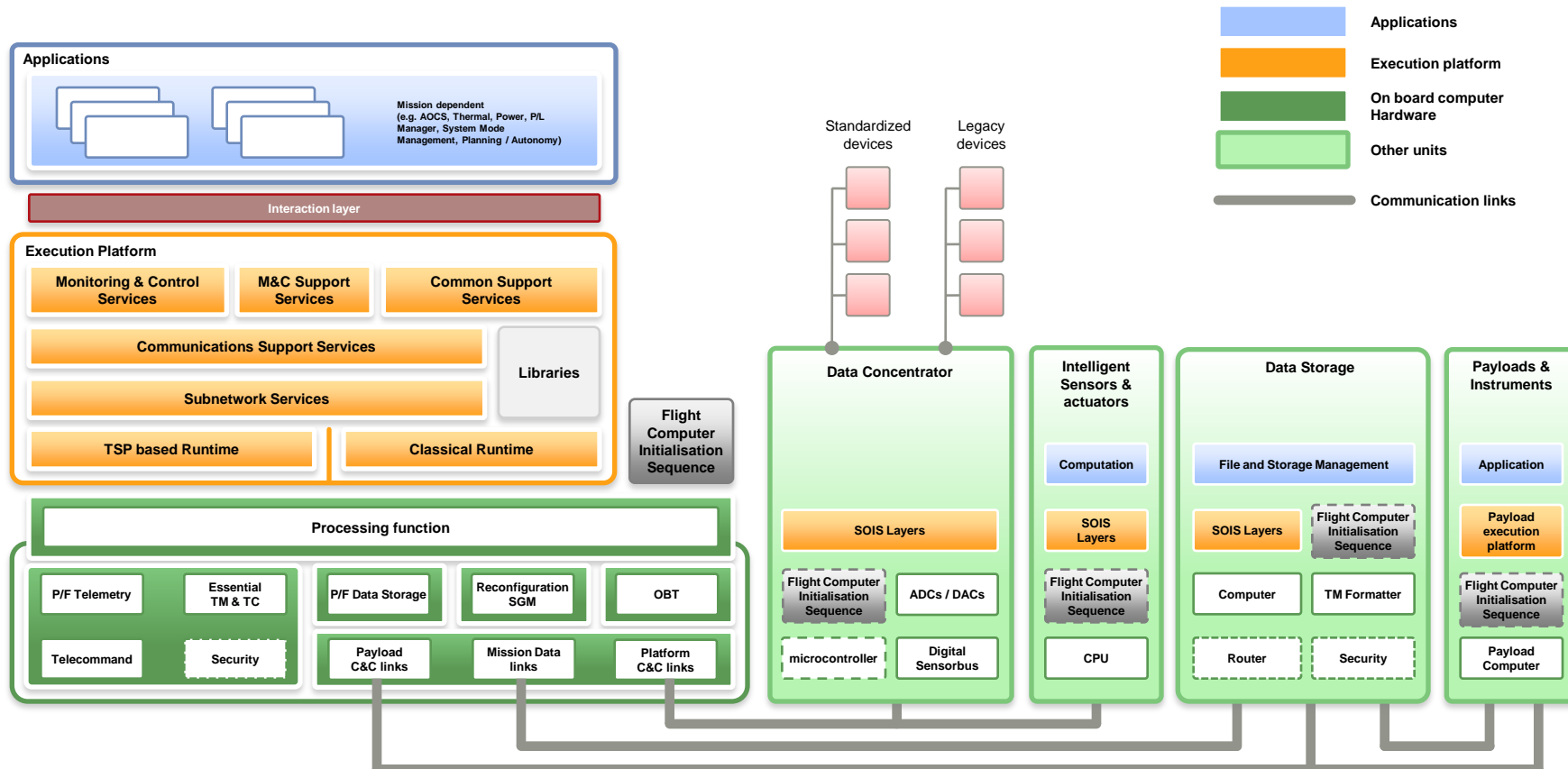




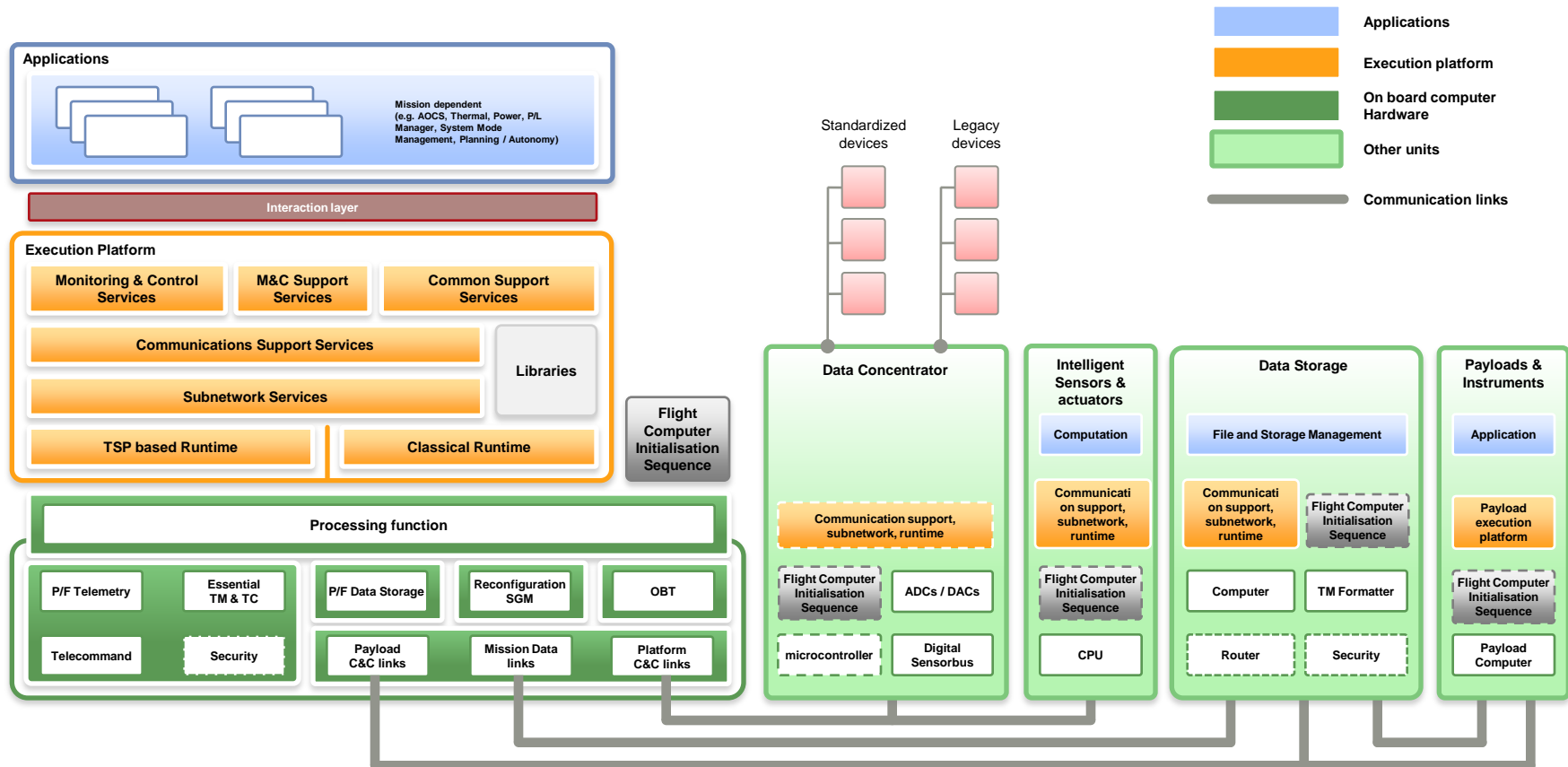
# The avionics reference architecture (HW + SW)



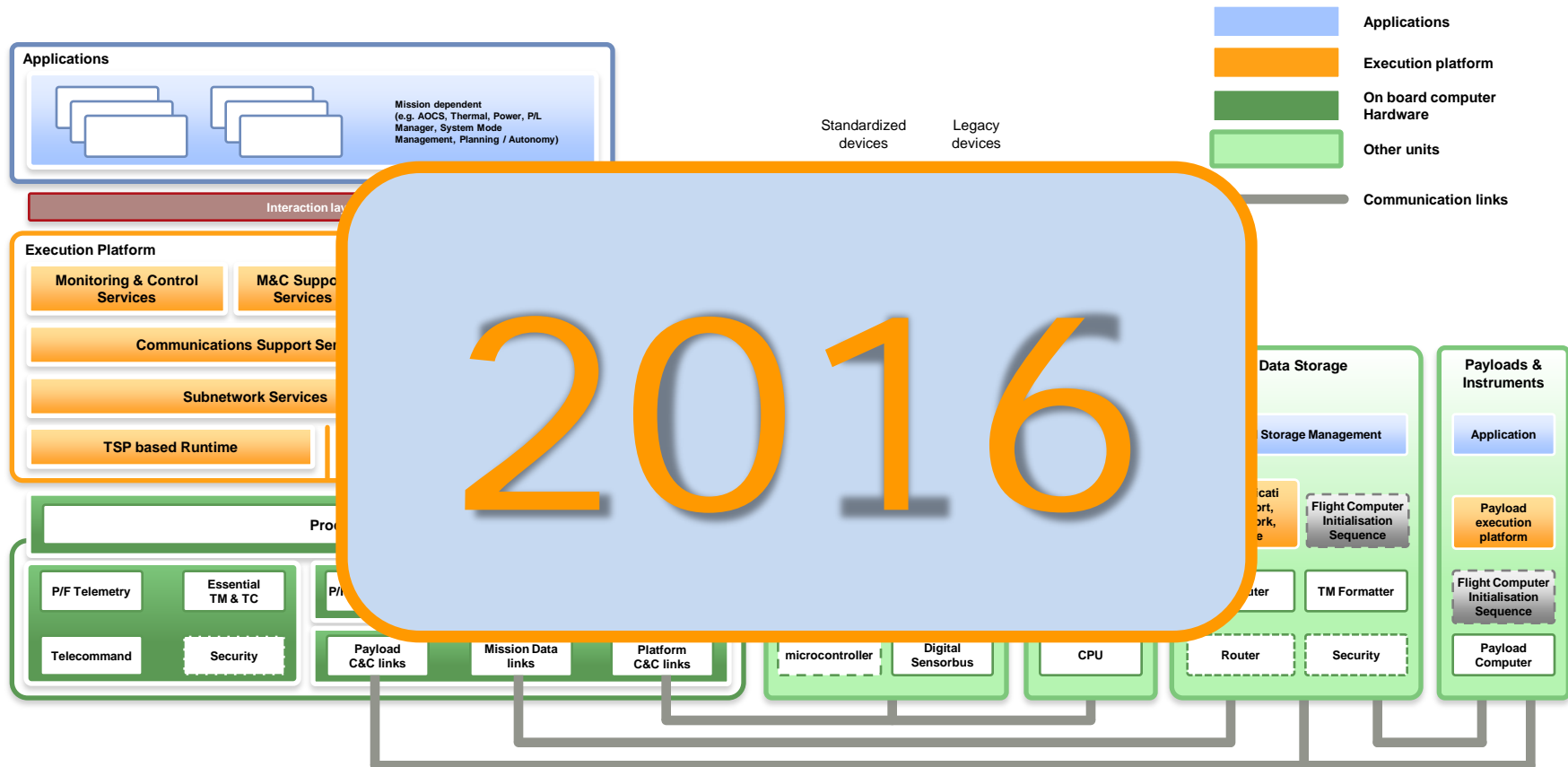
# The avionics reference architecture (HW + SW)



# The avionics reference architecture (HW + SW)



# The avionics reference architecture (HW + SW)



# SAVOIR Output



- reference avionics architecture
- interface specifications
- functional specification

Generic customer SRDs



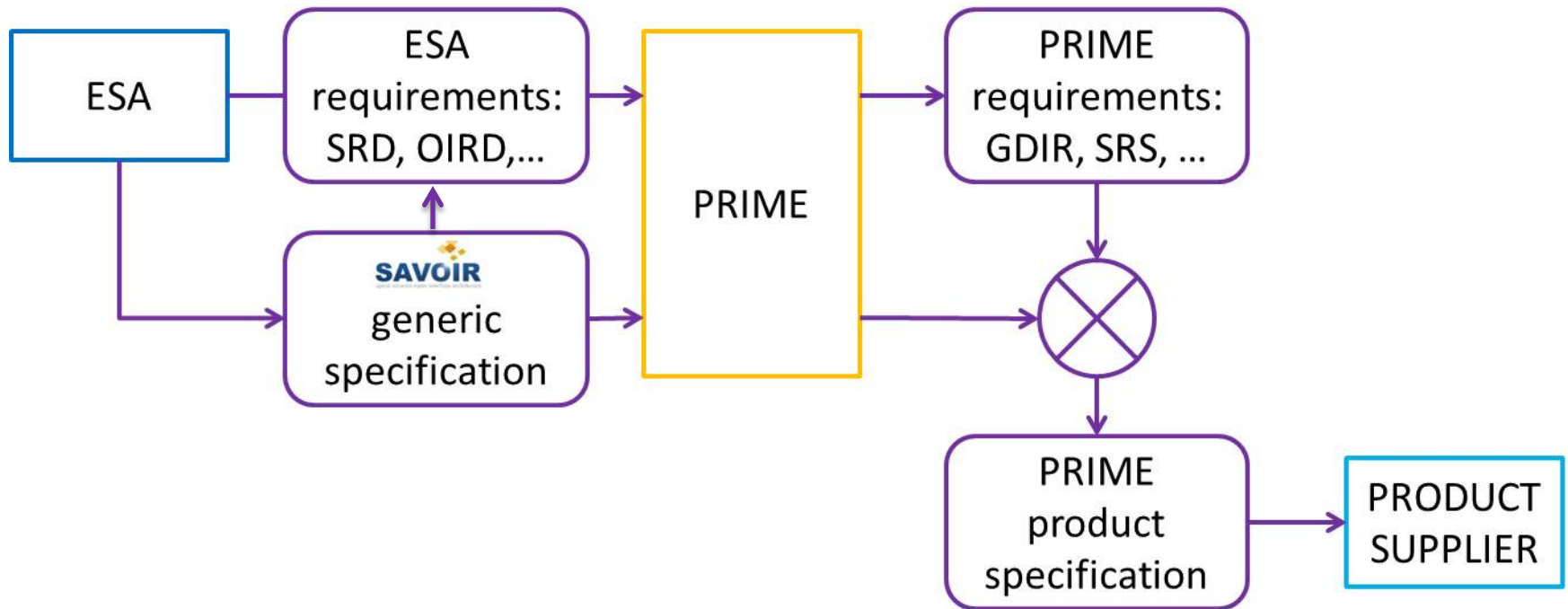
Generic System Integrator Product Spec



Product Lines



# Use of SAVOIR documents





# SAVOIR Documentation (1/4)



- SAVOIR Documentation tree SAVOIR-TN-000
- SAVOIR Functional Reference Architecture SAVOIR-TN-001



- SAVOIR Avionics System Reference Architecture Handbook SAVOIR-HB-002
- SAVOIR On-Board Software Reference Architecture Training Material SAVOIR-HB-001



# SAVOIR Documentation (2/4)

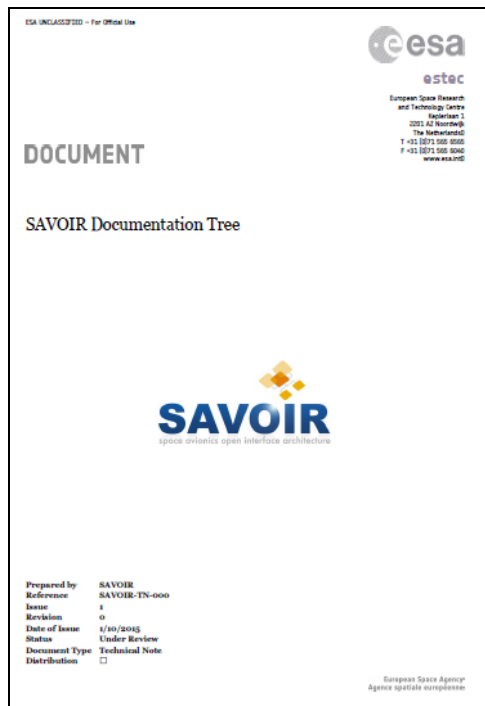


➤ SAVOIR Generic OBC Specification

SAVOIR-**GS**-001

➤ SAVOIR Flight Computer Initialisation Sequence Generic Specification

SAVOIR-**GS**-002



Working Group reports

- SAFI
- SAIF
- FAIRE
- IMA





- SAVOIR Generic RTU Functional and Operability Specification
- SAVOIR Generic Data Storage functional, performance, (*operational*) and interface specification
- SAVOIR Space Component Model
- SAVOIR Execution Platform Functional Interface



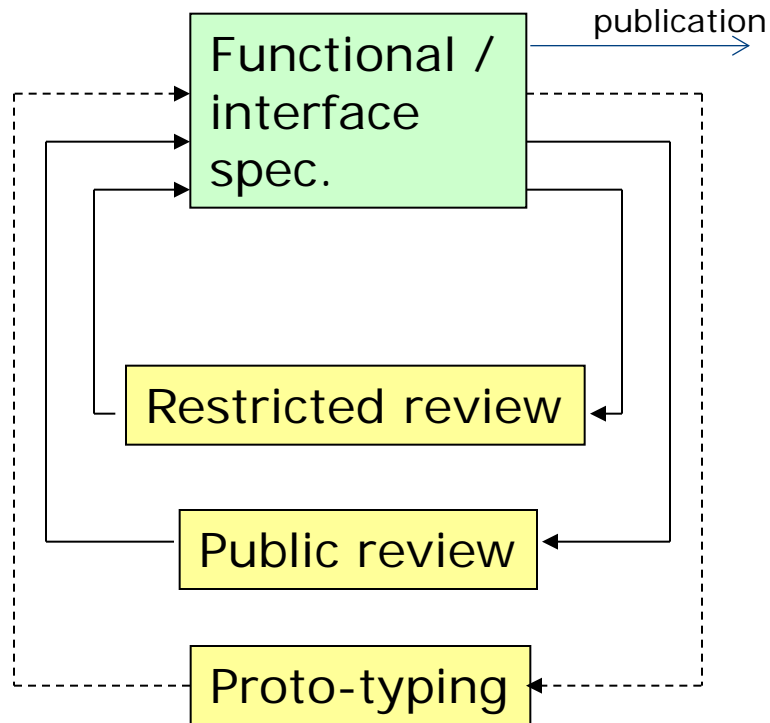


The list is intended to be up to date on:  
<http://savoir.estec.esa.int>

Available documents are released on the Esa Space  
Software Repository  
<http://essr.esa.int>



# Specification production scheme.



- Obc specification ✓
- Boot software specification ✓
- Generic OIRD ✗





Software reference architecture



Time and Space Partitioning



Sensor/Actuator Electrical interface Finalised



Sensor/Actuator Functional Interface Finalised



MAss Storage Access Interfaces and Services



Functional links



Fault Detection, Isolation, Recovery New





# TODAY



- Produce the Data Storage FMS generic specification 10:50
- Produce the Avionics Network generic specification 11:05
- Produce the Generic OIRD 11:20
- Publish the ASRA handbook 11:40
- Start the FDIR handbook and working group 12:00
- Produce the Electronic Data Sheet roadmap:  
from the SOIS concept to the prime industrial needs,  
through the Savoir-Safi experience 12:20
- Finalize the RTU generic specification  
and go for Public review 12:40





## Next year

- Finalize the software documents (OSRA: generic specification of an “execution platform” , component model approach), and go for Public review
- Tailored application, from platform to payload, of the
  - (i) OBC spec to ICU and
  - (ii) OSRA architecture to OSRA-P

→ payload reference architecture?





## Key Performance Indicators:

- Request to have Savoir transferred in CCSDS
- Assiduity to the SAG meetings
- High audience in dissemination events such as ADCSS
- Increasing maturity of Savoir documents
- ECSS and CCSDS are influenced by SAVOIR





## Deployment in projects

- Applicability as “Normative Documents”, something between requiring “Applicable Document” and only informative “Reference Document”.
- Unformal application in Euclid
- More formal application in ESA instrument FLEX, and PLATO





### Challenge

Harmonizing the way the Esa ITT are done up to the lowest procurements

→ generic SRD

**“life style”**



# Contact



Feedback: [savoir@esa.int](mailto:savoir@esa.int)

<http://savoir.estec.esa.int>

<http://essr.esa.int>



## SAVOIR Advisory Group:

- Jean-Loup Terraillon – ESTEC/TEC-S
- Kjeld Hjortnaes – ESTEC/TEC-SW
- Philippe Armbruster – ESTEC/TEC-ED
- Alain Benoit – ESTEC/TEC-EC
- Juan Miro – ESOC/OPS-G
- Jean-Noel Bricout – CNES
- Frank Dannemann – DLR
- Rémi Roques – AirbusDefence&Space
- Jacques Busseuil – ThalesAleniaSpace
- Bernard Bruenjes- OHB
- Carsten Jørgensen – Terma
- Torbjörn Hult – RUAG
- Franco Boldrini – Selex Galileo

