TEC-ED & TEC-SW Final Presentation Days
May 2017

Activity Title:	
High Capacity, High Speed IC Test System for Automatic Fault Injection and Analysis (FT-UNSHADES2) Presentation: FT-UNSHADES maintenance	
Budget: 30 K€	Program Line: TRP, CCN2 4200022981/09/NL/JK
Duration: 20 months	Kick Off Date: 1/April/2015
TO: David Merodio Codinachs (TEC-EDM)	
Prime: AICIA, Andalusian Association for Research and Industrial Cooperation (ES)	

Main Objectives:
FT-UNSHADES2 is a non-instrumented, FPGA-based fault injection emulator platform composed of hardware, FPGA-designs, embedded software and server software and web components. The main objectives of the CCN2 are:

- General maintenance of FT-U2 system and support to users
- Implementation of new features. For example Analog-FTU and support for injection in the BRAM
- First experience towards using FT-U2 for both radiation experiments and fault-injection

European Space Agency