## ADCSS2017 - Day 3 Space Processors: Round-Table

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From conceptual needs expressed by potential costumers to availability of qualified silicon solutions a gap of 10 years seems to be the norm, which means do we have to reduce this period considering the increased complexity of solutions but also the necessity to have solutions in a reduced time? Or can 10 years be accepted as an unavoidable period for a development for space?

Shall we aim for 10x, 100x or 1000x improvement?



COTS solutions in Space, we have been always using COTS in space in the past in order to implement functionalities not provided by Space RAD-Hard devices but the trend is increasing pushed also by costs reduction needs and early availability of solutions. ESA is trying to consider all the aspects related to the use of COTS devices (reliability, radiation characterization, availability of LOTs, reference designs, adoption of quality standards...) collecting the recent experience and including in an a higher and wider scenario all the various initiative we have recently started or under finalization. A dedicated workshop on COTS (with a longer round-table...) will be done in 2018 but could you anticipate what are you expecting from ESA in the area of COTS?



Which is industry/project opinion, for future processors, regards SMP/AMP on identical cores (the NGMP or DAHLIA approach) vs. HEMP (that is heterogeneous multiprocessing) and single cores (at higher clocks) for space systems.