## *RISC-V: The open processor architecture that is here to stay*

*by Mr. Jonathan Hofman (Technolution, NL)*

RISC-V is an open, scalable RISC based processor architecture that is the result of over 30 years of research at Berkeley University. Today, with its rapidly growing and active community and backed by a foundation that has over 50 members including Google, Samsung, AMD and IBM, RISC-V can't be ignored anymore. A good processor requires a good ecosystem. This philosophy, which is a struggle for many open source processor initiatives, is well understood by ARM. RISC-V fills in the gap felt by many silicon and embedded companies that require the possibility to tweak their processor designs for a certain domain. Technolution has developed a RISC-V core targeting security and safety critical applications. In this talk we will dive deeper into the considerations of Technolution for choosing RISC-V and becoming a founding member of the RISC-V foundation. We will discuss the ecosystem and the software tools we use, and we will provide an insight of our roadmap to the future.