

Space Processors

Today's Agenda



NOW: KeyNote: The SPARC history in SPACE

09:55 - 10:35 Space Processors: Programs Views

10:35 - 11:15 Space Processors: Primes Views

11:15 - 11:40 Coffee Break

11:40 - 14:40 Space Processors (with Lunch Break @ 13:00 ... 1h0')

14:40 - 15:20 Space Processors: Emerging Technology

15:20 - 17:10 Space Processors: Programming multicore and heterogeneous architecture (with Coffee Break 15' @ 15:40)

17:10 - 17:30 Space Processors: Conclusions, Round Table, Wrap-up

Keynote

JIRI GAISLER

Dwarfs standing on the shoulders of giants.

designlines AUTOMOTIVE

News & Analysis

European Space Agency launches free Sparc-like core

European Space Agency launches free Sparc-like core

Peter Clarke

3/6/2000 10:23 PM EST

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LONDON — Open-source licensing of 32-bit processor cores is gaining ground, as the European Space Agency recently made available its Leon-1 CPU core. Though the core has yet to be fully tested in hardware, its VHDL source code has been downloaded at least 150 times since Feb. 17 when the debugged core went up on the agency's Web site.

Keynote

JIRI GAISLER

Some observers suggest that the free availability of Leon-1 and of another 32-bit architecture, [OpenRISC 1000](#), could undermine commercial core licensors like ARM Ltd., MIPS Technologies and Sun Microsystems. Indeed, Leon's designer, Jiri Gaisler, said he's already been approached by "a very big mobile phone company" looking to cut processor costs. He would not name the company.

A spokeswoman for ARM Ltd., a leading licensor of processor cores, downplayed the impact of the free cores on her company's business.

By offering Estec's implementation of the Sparc architecture under the LGPL scheme, Gaisler hopes to make the core acceptable to commercial organizations that could then add proprietary cores to produce system-chips.

Keynote

JIRI GAISLER

SPARC in SPACE.

ALL current flying, developing and planned ESA missions fly **SPARC**-based Rad-Hard by Design processors.

Atmel sold ~**20000** ERC32 single chip

Overall ~**10000** LEON2/3 (between AT697F and SoC ASIC/ FPGAs) have already been sold.

Coming next



09:55 - 10:35 Space Processors: Programs Views

09:55 Science (Euclid) - Mr. Luca Stagnaro (ESA/ESTEC)

10:15 Earth Observation - Mr. Josep Rosello (ESA/ESTEC)

10:35 - 11:15 Space Processors: Primes Views

10:35 Airbus DS view - Mr. Olivier Notebaert (Airbus DS)

10:55 Thales Alenia Space view - Mr. Antoine Provost-Grellier (Thales Alenia Space)