

11th ESA Workshop on Avionics, Data, Control and Software Systems (ADCSS2017)

Thursday, 19 October 2017

Space Processors: High Performances Symmetric/Asymmetric Multicore - Newton Conference Center (11:40 - 14:40)

time	[id] title	presenter
11:40	[41] GR740 and GR716 development status and applications	Mr ANDERSSON, Jan
12:00	[42] DAHLIA: Deep sub-micron microprocessor for spAce rad-Hard application Asic	Mr PROVOST-GRELLIER, Antoine Mr POUPAT, Jean-Luc
12:20	[43] LeanOS - An Operating System for the SSDP	Mr LUNTZER, Armin
12:40	[44] RC64™—Rad hard high performance DSP manycore	Mr GINOSAR, Ran
13:00	Lunch Break	
14:00	[45] Atmel/Microchip Products	Mr GANRY, Nicolas
14:20	[46] Development of Next-generation Space-Grade Processors, SOI-SOC series	Mr NARITA, Takanori