

11th ESA Workshop on Avionics, Data, Control and Software Systems (ADCSS2017)

Thursday, 19 October 2017

Space Processors: Programming multicore and heterogeneous architecture - Newton Conference Center (15:20 - 17:10)

time	[id] title	presenter
15:20	[49] Mission critical application based on the Control Loop Processor: architecture and development flow of the Ariane 6 P120 Thrust Vector Control system embedded software	Mr FERON, Jean-Brieuc
15:40	Coffee Break	
15:55	[50] Executing Parallel Real-time Software on Multi- and Manycores in a Timely Manner	Mr CAZORLA, Francisco J. Mr KOSMIDIS, Leonidas
16:15	[51] MTG Lightning Imager: how to detect lightning from space and why on-board processing power matters	Mr KOKOU, Pierre
16:35	[52] Introduction to Dyplo and its latest status regarding support for rad-hard FPGAs and RTEMS	Mr JACOBS, Roger
16:55	[53] The Compact Reconfigurable Avionics	Mr HONVAULT, Christophe ODDENINO, Davide Mr ILSTAD, Jørgen