

Analogue and **M**ixed-Signal **I**ntegrated **C**ircuits for **S**pace **A**pplications

AMICSA 2006

AMICSA 2008

AMICSA 2010

AMICSA 2012

AMICSA 2014

AMICSA 2016

AMICSA 2018

HOW THINGS EVOLVED

https://www.esa.int/Our_Activities/Space_Engineering_Technology/Microelectronics/Analogue_and_Mixed-Signal_IPs_ASICs

- 5 ADCs
- 4 DACs
- 1 LVDS repeater
- 2 microcontrollers with analogue functions
- overview diagram of ESA funded analogue IPs and devices

More info / corrections: David Levacq (ESA TEC-EDM)

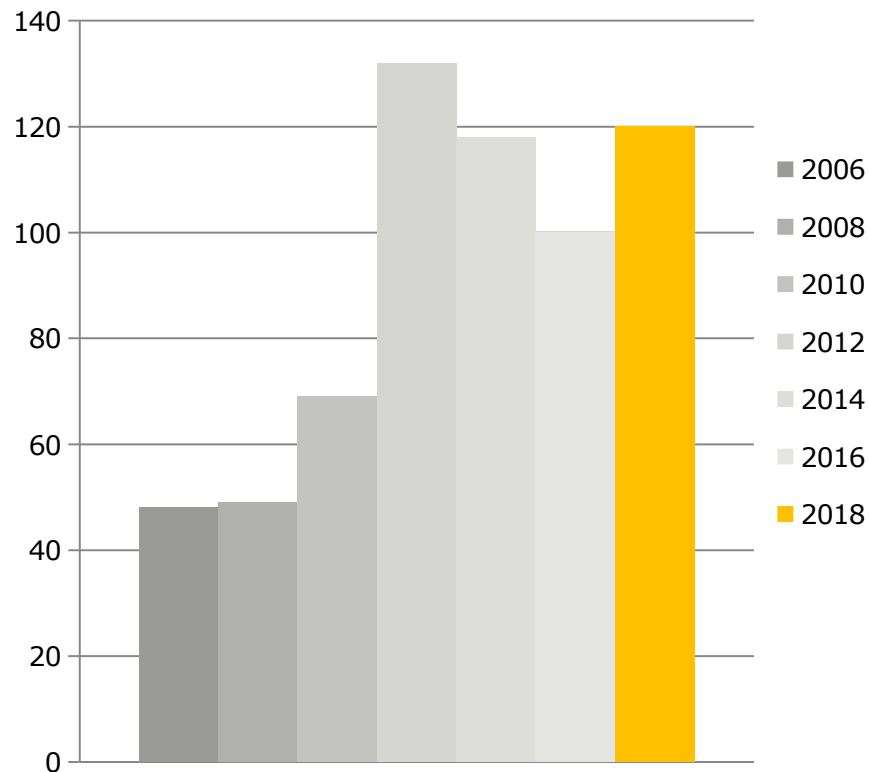


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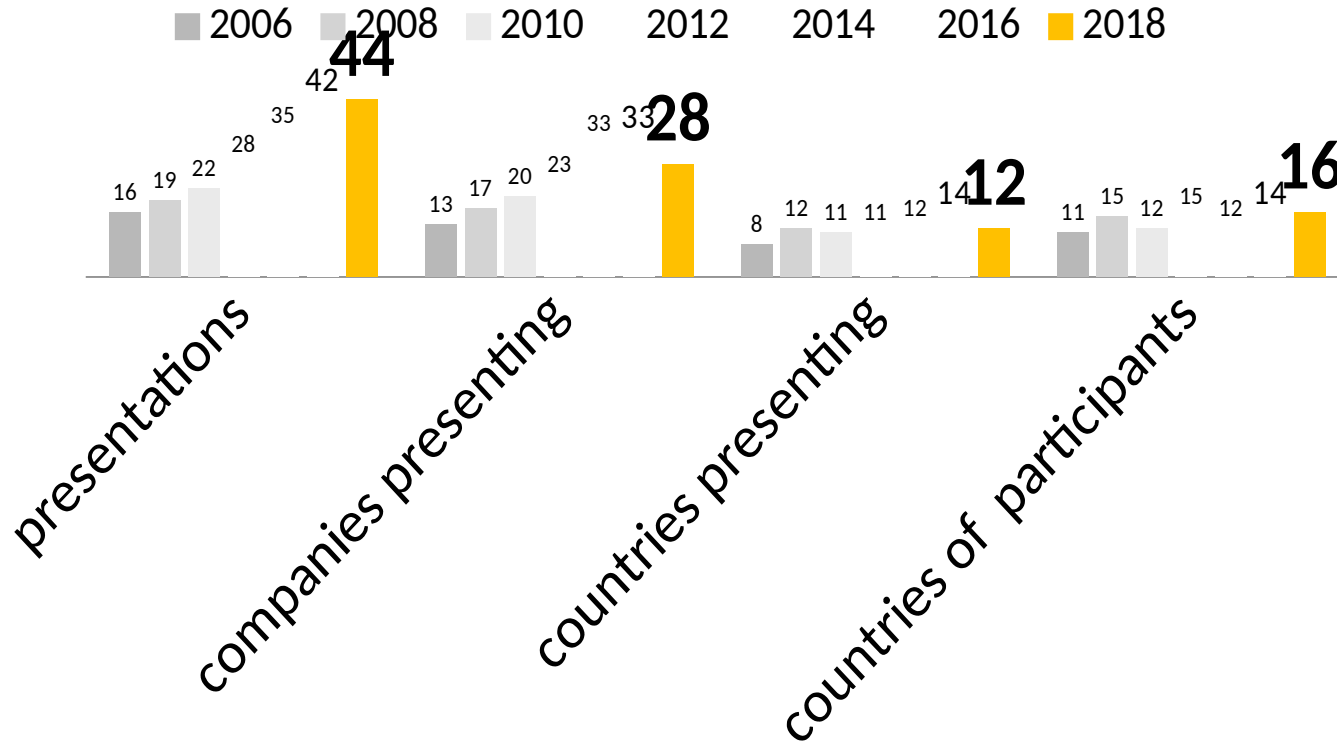
| wrap up | 20/0

REGISTERED PARTICIPANTS

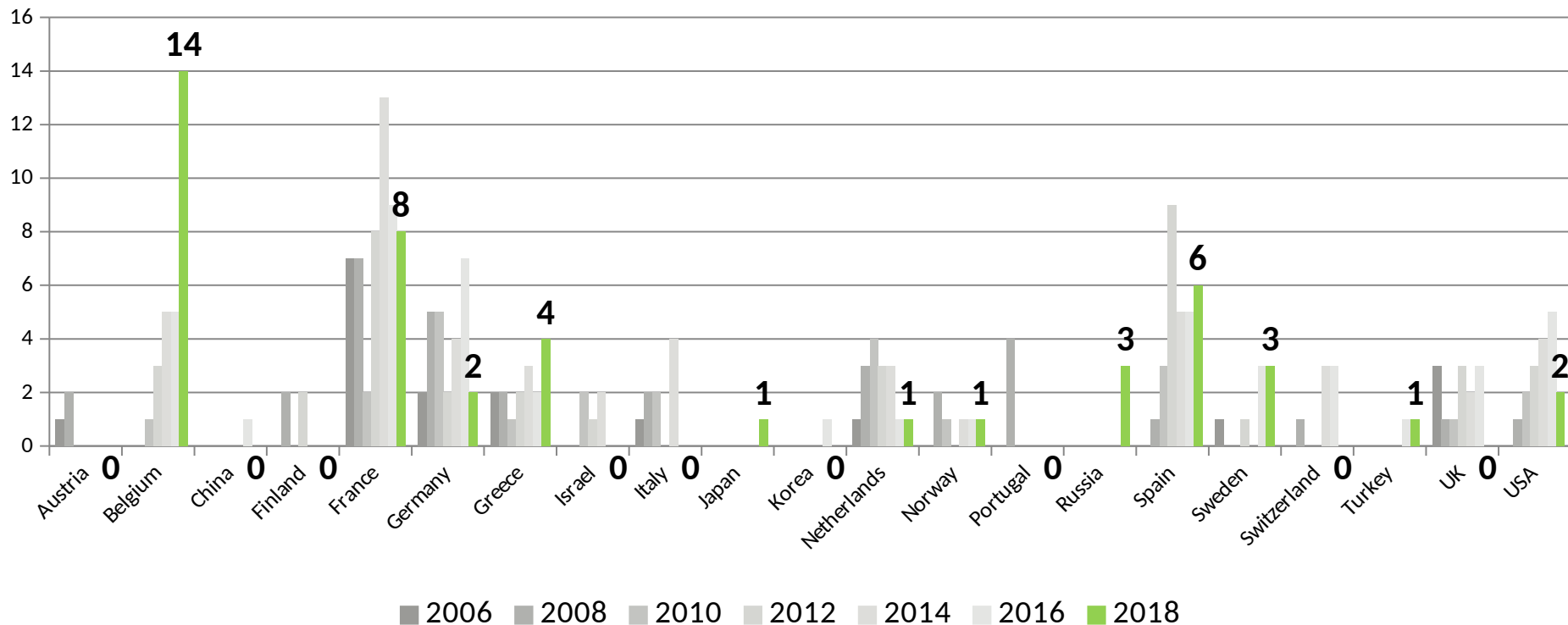
129



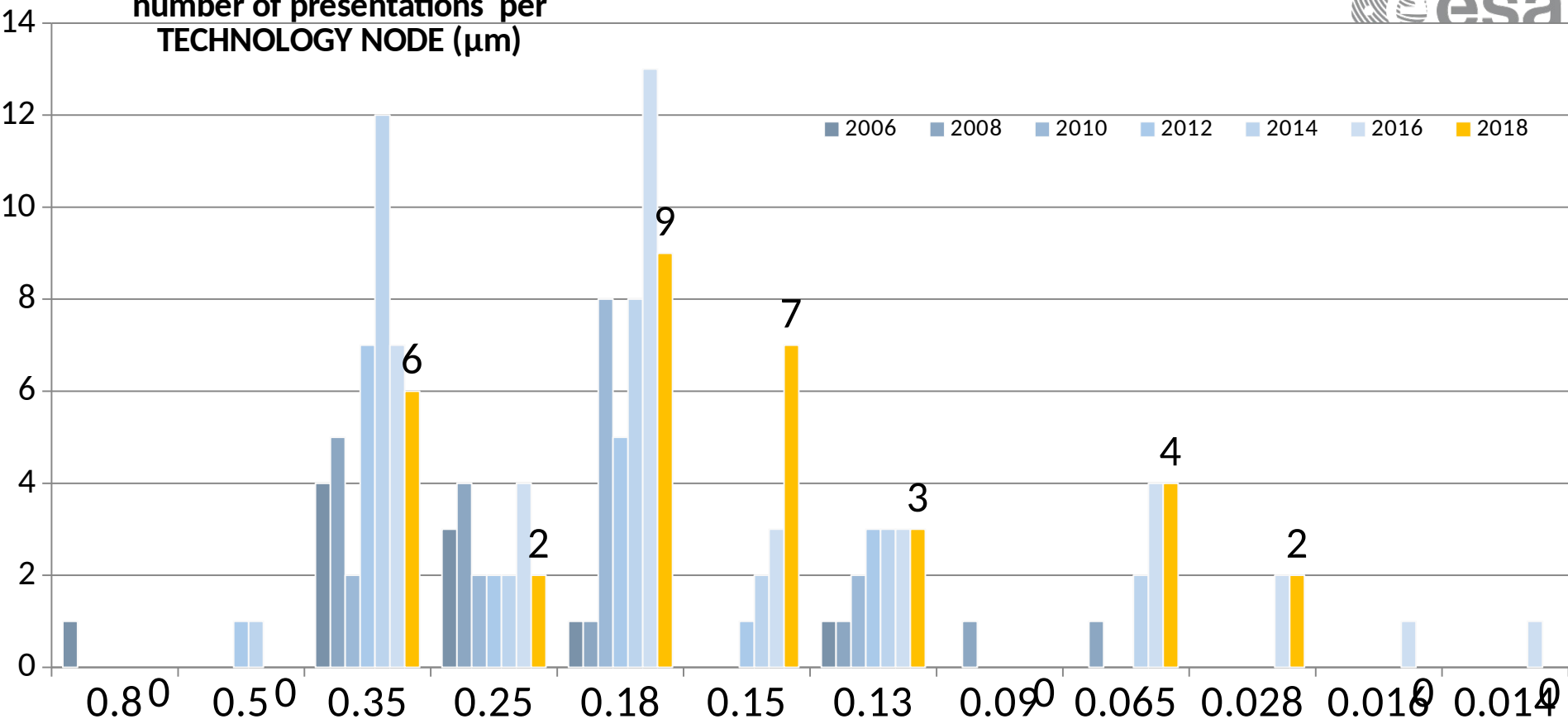
Number of presentations, companies and countries



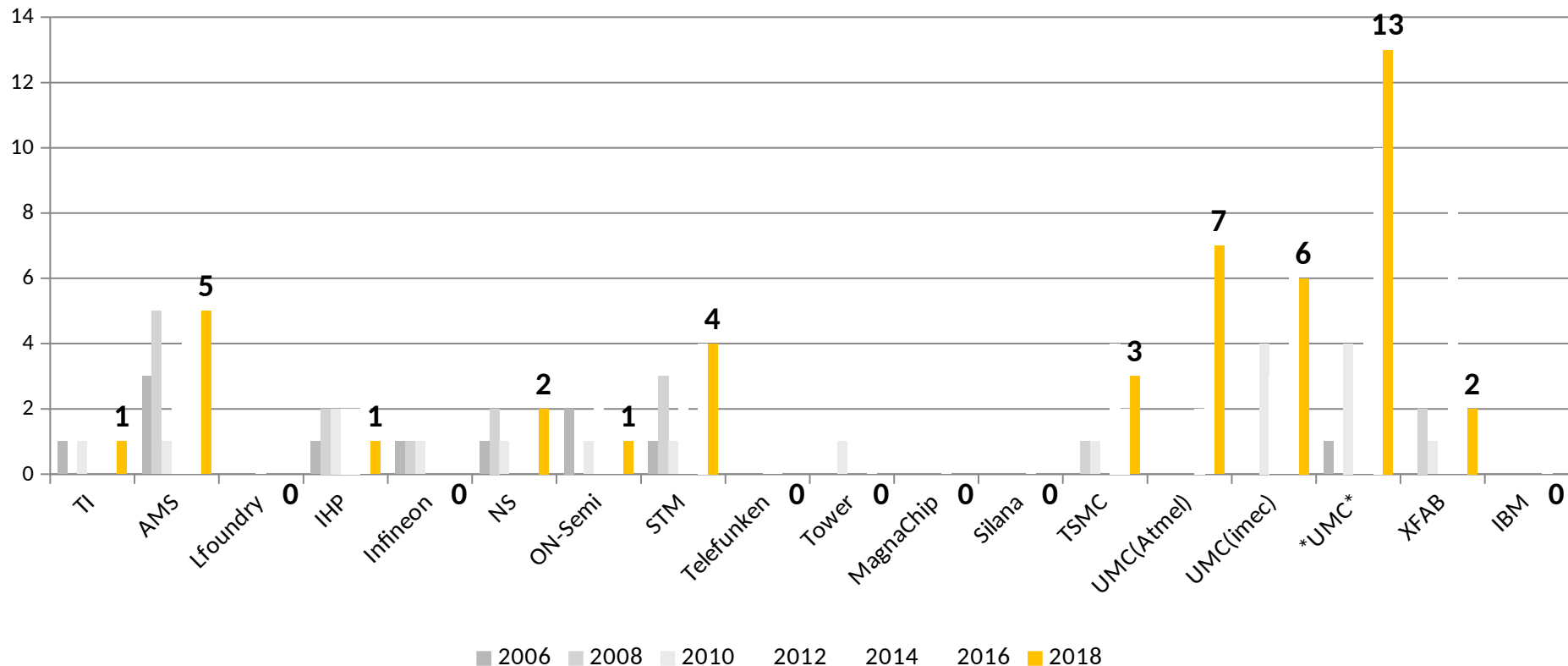
number of presentations per COUNTRY



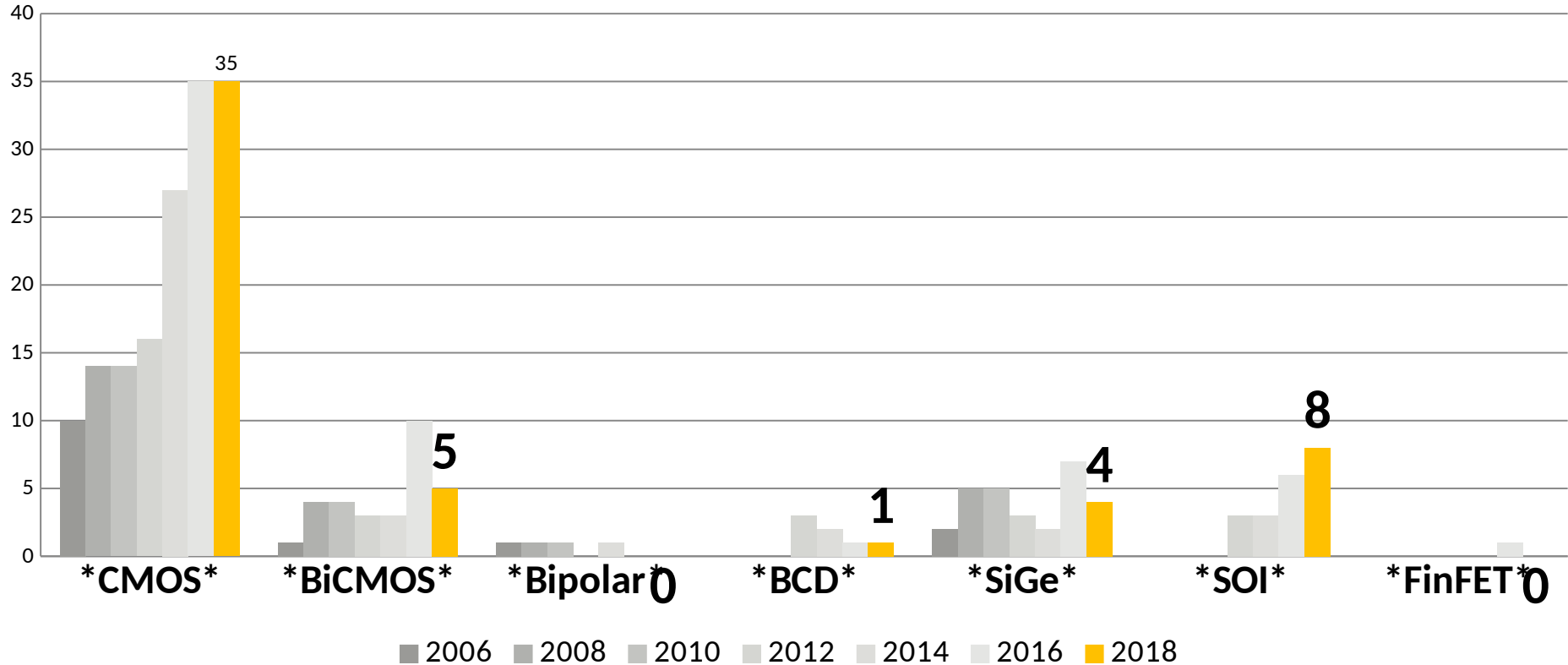
number of presentations per TECHNOLOGY NODE (μm)



number of presentations per FOUNDRY



number of presentations per TECHNOLOGY TYPES



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A. Fernández-León | ESA Microelectronics Section | AMICSA 2018 | wrap up | 20/0



European Space Agency

AMICSA: 12 years on, 7 editions



2006

U. Thrace, Ξ
(Xanthi)
Greece



2014

CERN,
Genève
Switzerland



2020



2008

Cascais/
Sintra
Portugal



2016

Gothenburg
Sweden



**2010,
2012**

ESTEC,
Noordwijk
Netherlands



2018

Leuven
Belgium

THANKS !

and see you at

AMICSA 2020



