

European Workshop on On-Board Data Processing (OBDP2019)

Wednesday, 27 February 2019

Devices and IP for On-Board Data Processing - Erasmus (09:00 - 12:50)

-Conveners: Agustin Fernandez Leon

time	[id] title	presenter
09:00	[70] Microchip ARM Solutions for Space - From COTS to Rad-Hard by Design System-on-Chips	GANRY, Nicolas
09:20	[34] Teledyne e2v Radiation Tolerant COTS+ Space Microprocessors - De-risking the introduction of ARM based solutions	GUILLEMAIN, Thomas Mr BALL, Mikael
09:40	[84] NX RHBD FPGA solutions for OBDP applications	LE MAUFF, JOEL
10:00	[59] Using Heterogeneous Computing on GPU Accelerated Systems to Advance On-Board Data Processing	Mr TSOG, Nandinbaatar
10:20	[19] High Performance Data Processor (HPDP) - Image Processing Applications of a New Generation Space Processor	Mr HELFERS, Tim Mr PAPADAS, Constantin
10:40	[64] RC64-Based NOGAH Systems: Enabling Software Defined Satellite Payloads	GINOSAR, Ran
11:00	Coffee Break	
11:30	[35] Reliable multi-processor hardware design	Dr RAUWERDA, Gerard
11:50	[25] ASIC solution for Software Defined Radio regenerative payload	Mr GAZELLE, David Mr ABRAHAM, Thierry
12:10	[20] Fast Fourier Transform at 100 Msamples/sec with the SkyFFT ASIC	VOLLMULLER, Bert-Johan
12:30	[31] Embedded GPU benchmarking for High-Performance On-board Data Processing	Dr KOSMIDIS, Leonidas