SEFUW: SpacE FPGA Users Workshop, 4th Edition

Tuesday, 10 April 2018

FPGAs: High Performance - Newton 1 and 2 (15:20 - 16:20)

-Conveners: Lucana Santos

time	[id] title	presenter
15:20	[32] Review and comparison of design methodologies and hardware implementations on FPGA technologies. Case study: CCSDS compression algorithms for multispectral and hyperspectral images	Dr SÁNCHEZ, Antonio Mr BARRIOS, Yúbal
15:40	[9] Image compression on reconfigurable FPGA for the SO/PHI space instrument	Mr HERNANDEZ EXPOSITO, David
16:00	[4] Implementation of Visual Based Navigation in a CPU-FPGA architecture for planetary landing	Mr OLIVEIRA, Joao

FPGAs: High Performance - Newton 1 and 2 (16:50 - 17:10)

-Conveners: Lucana Santos

time	[id] title	presenter
16:50	[50] Implementation of a GNSS Space Receiver on a Zynq	Mr MAJORAL, Marc