

Porting Eagle-Eye to an ARM-based Processor

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European Space Agency

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- *Budget:* 200.000 €
- *Duration:* 12 months
- *Prime:* Terma GmbH
- *Main Objectives:*
 1. To study different ARM processors and their SoC implementations in order to select products that might be well suited for space on-board computers;
 2. To create a configuration of the Eagleeye CSW and the related ATB infrastructure allowing to use the ARM Cortex-R processor (ARMv7-R architecture);
 3. To adapt the DMS and payload partitions from the OBOSS framework to the LVCUGEN PUS library.