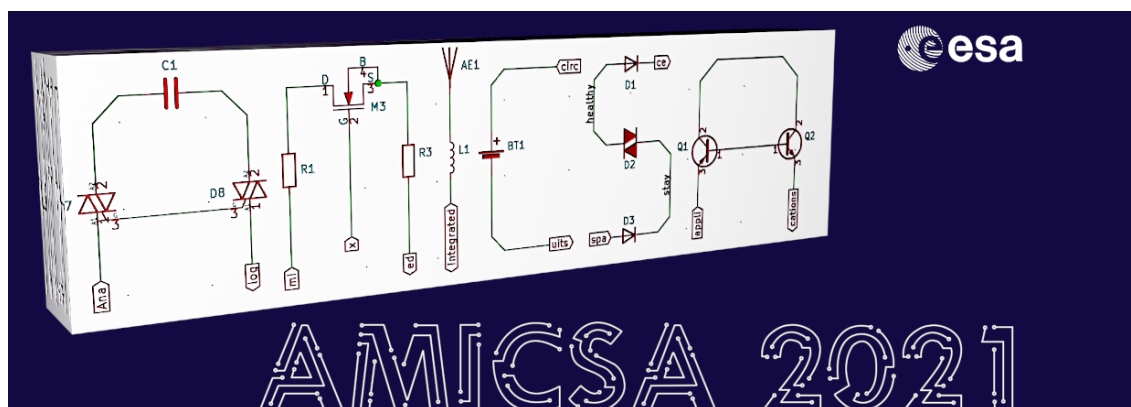


Session Program

May 25 - 28, 2021



AMICSA 2021

Analogue intellectual property and re-usability of analogue circuits in space

Online
Virtual Event

Tue, May 25

4:55 PM

Analogue intellectual property and re-usability of analogue circuits in space: Analogue intellectual property and re-usability of analogue circuits in space (1/2)

Session | **Location:** Online, Virtual Event

4:55 - 5:15 PM

ATMX150RHA Building Blocks - Block Presentation and Measurement Results

Speaker

Julien Fleury

5:20 - 5:40 PM

Validation of an Analog Mixed-Signal Library

Speaker

Mr Kostas Makris

5:40 PM

Wed, May 26

1:30 PM

Analogue intellectual property and re-usability of analogue circuits in space: Analogue intellectual property and re-usability of analogue circuits in space (2/2)

Session | **Location:** Online, Virtual Event

1:30 - 1:50 PM

PROMISE, PROgrammable MIXed Signal ASIC Electronics Framework

Speaker

Mr Luis Berrojo

1:55 - 2:15 PM

Integration of two High-Performance Mixed-Signal Data Conversion IPs

Speaker

Dr Gildas Leger

2:20 - 2:40 PM

Functional Validation and Evaluation of the MAS-FE testchip

Speaker

Luka Veljovic

2:40 PM