

AMICSA 2021

Tuesday, 25 May 2021

Analogue intellectual property and re-usability of analogue circuits in space: Analogue intellectual property and re-usability of analogue circuits in space (1/2) (16:55 - 17:40)

time	[id] title	presenter
16:55	[40] ATMX150RHA Building Blocks – Block Presentation and Measurement Results	FLEURY, Julien
17:20	[10] Validation of an Analog Mixed-Signal Library	Mr MAKRIS, Kostas

Wednesday, 26 May 2021

Analogue intellectual property and re-usability of analogue circuits in space: Analogue intellectual property and re-usability of analogue circuits in space (2/2) (13:30 - 14:40)

time	[id] title	presenter
13:30	[23] PROMISE, PROgrammable Mixed Signal ASIC Electronics Framework	Mr BERROJO, Luis
13:55	[22] Integration of two High-Performance Mixed-Signal Data Conversion IPs	Dr LEGER, Gildas
14:20	[27] Functional Validation and Evaluation of the MAS-FE testchip	VELJOVIC, Luka