

SEFUW: Space FPGA Users Workshop, 5th Edition

Tuesday 17 March 2020

Industrial Experiences - Newton 1 and 2 (10:00 - 11:40)

time	[id] title	presenter
10:00	[36] Challenges with New FPGA Technologies	TEJERINA BUSTILLO, Isaac
10:25	[35] MPSOCS FOR RECONFIGURABLE MODULAR SPACECRAFT	ROWLINGS, Matthew
10:50	[14] Development and FPGA Roadmap for LEON5FT and NOEL-V Processor Models	Mr RONNBACK, Martin
11:15	[40] SpaceFibre and SpaceWire IP cores for radiation tolerant FPGAs	Dr MCCLEMENTS, Chris PARKES, Steve

Thursday 19 March 2020

Industrial Experiences - Newton 1 and 2 (14:50 - 16:30)

time	[id] title	presenter
14:50	[32] First Design-In Experiences and Comparison of KU060, NG-ULTRA and RTPolarFire for On-Board Processing and AI	Dr BEDI, Rajan
15:15	[8] Flexible FPGA Architecture for Compact High-Performance PDHU and OBC Mass Memory	Dr ZHOU, Gang
15:40	[28] Xilinx Zynq Ultrascale+ MPSoC-based modular unit for rapid spacecraft hardware prototyping	Mr WALSH, Michael
16:05	[17] Designing a low-latency Fail-operational motor controller for space on industrial-grade FPGAs	WOLFF, Alois