

Open Standards – A prerequisite to configure a satellite data handling system

ADCSS 2020

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DEFENCE AND SPACE

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TESO Avionics Team

*“Where **open standards** exist, **innovation** is driven; disruptive technologies emerge. Things become **more valuable, smarter and easier to use**. This pattern has been shown time and time again in almost every industry on the planet. The examples are endless. Open standards foster a **broad selection of products and vendors** for end users to choose from. This competition is what drives innovation. More importantly, open standards allow small and medium-sized companies to compete. **Disruptive technologies** often emerge from such companies that are agile enough **to innovate** based on open standards. At the end of the day, the **end user wins** by not being locked into one large company’s method of doing business.”*

What others say ...



May 10th, 2017,
Michael Bowne,
Executive director,
PI North America

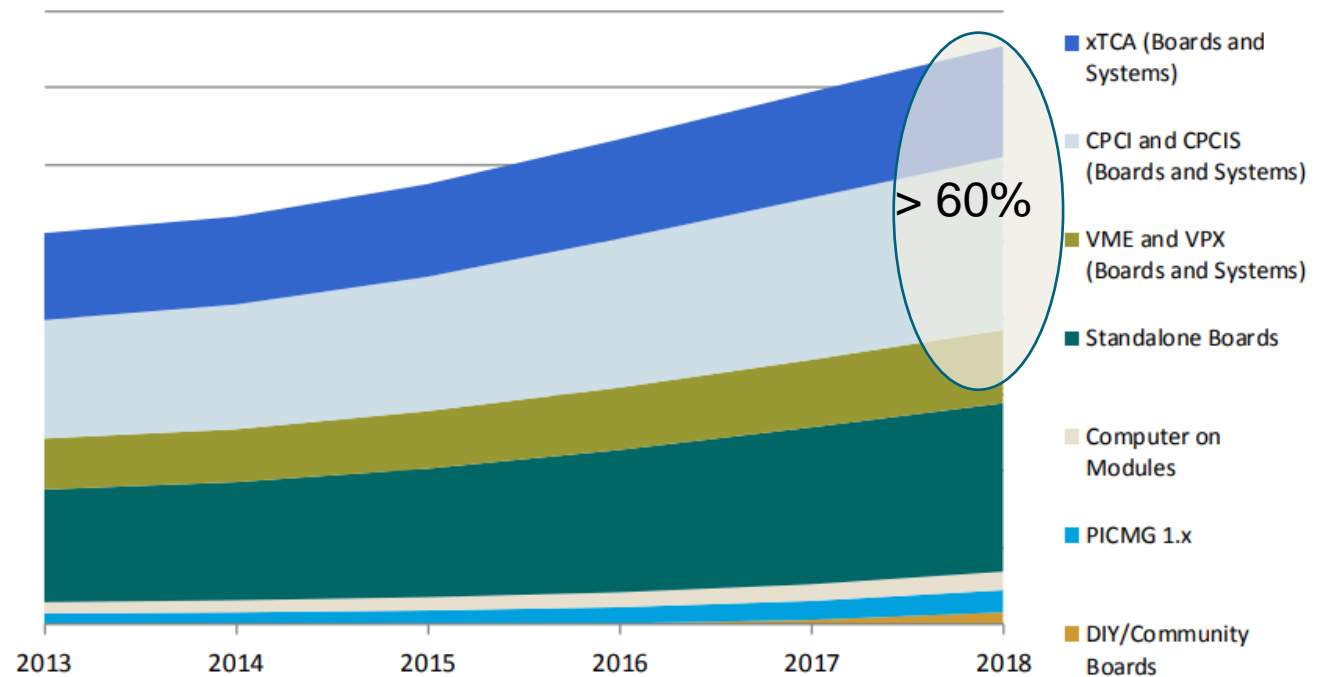
What exactly is an open standard?

- Many organization, consortiums and institutes,
 - Relevant for our business
 - CCSDS
 - ECSS
 - IEEE
 - VITA
 - PICMG (PCI Industrial Computer Manufac

- Open standards are based on five principles
 - Cooperation
 - Adherence to principles
 - Collective empowerment
 - Availability
 - Voluntary adoption

The world merchant embedded computing market by technology

Revenues (\$M) - 2013 to 2018



Source: IHS

What is ADHA (Advanced Data Handling Architecture)?

- ESTEC initiative to foster modularity in space avionics

Two consortiums: TAS and Ruag/Airbus

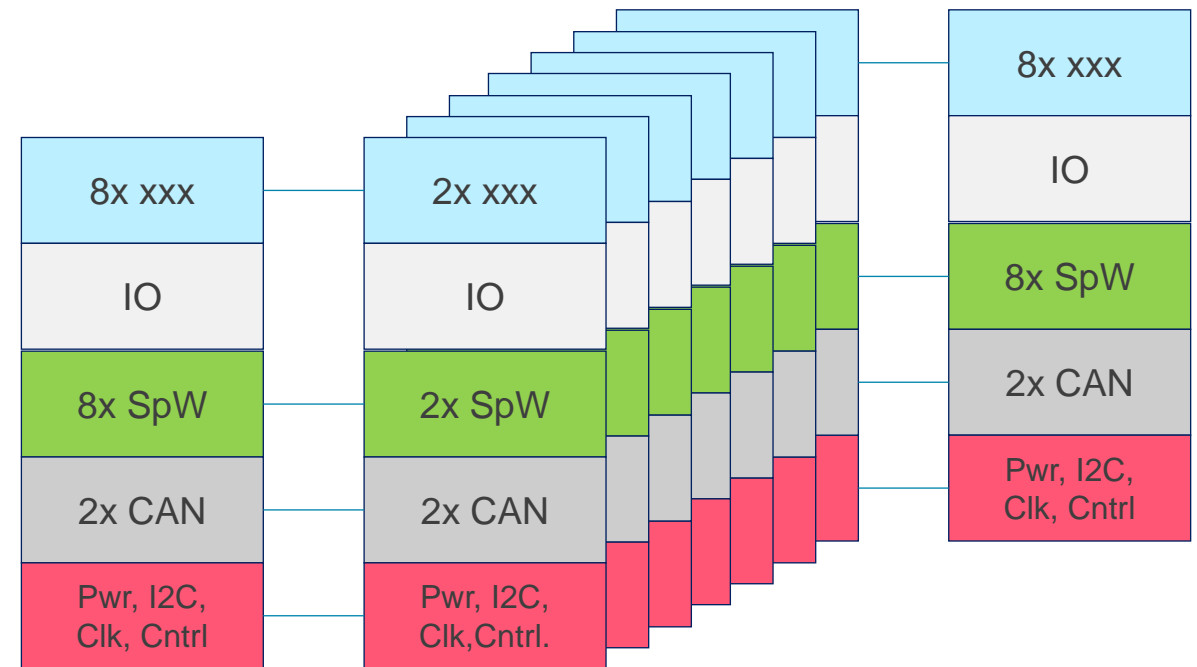
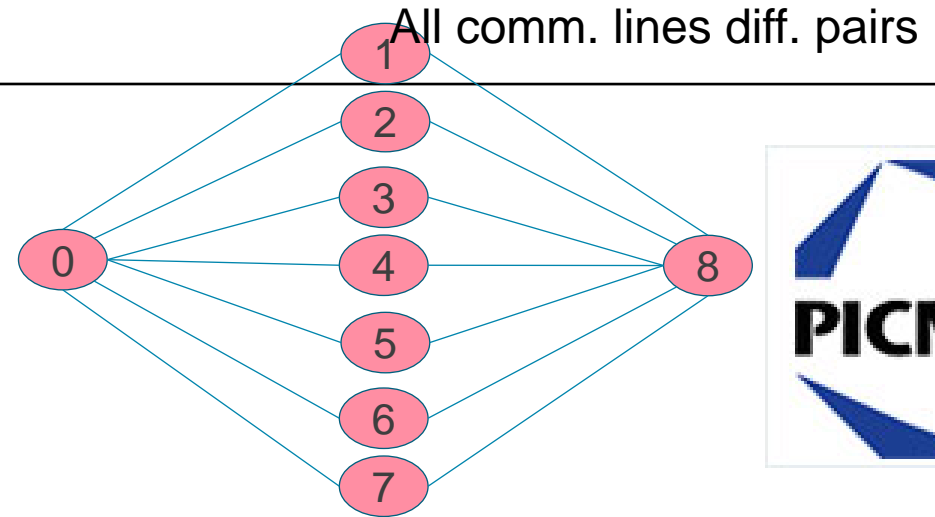
Two Standards: spaceVPX and CPCI Serial Space



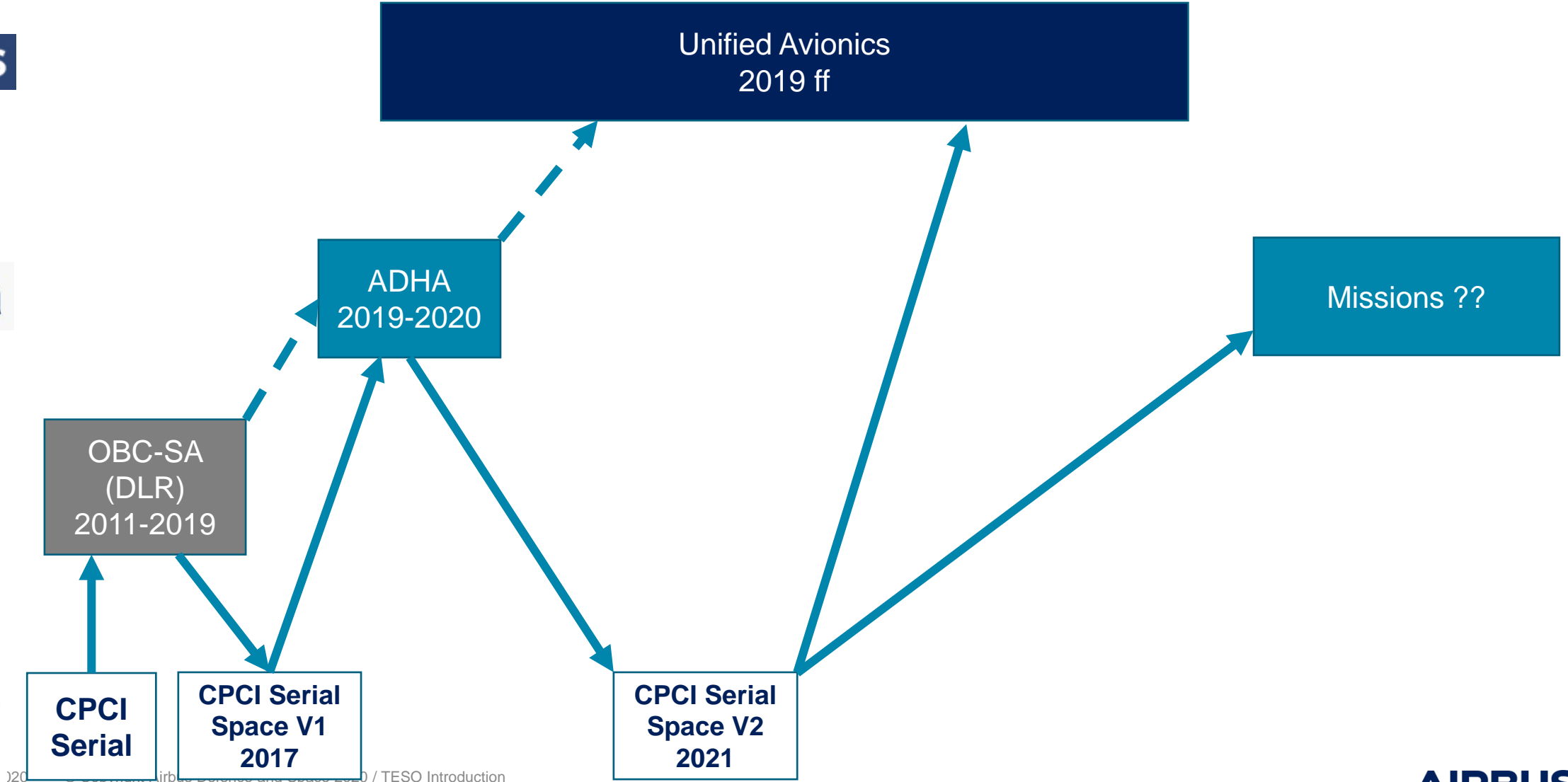
What is CPCI Serial Space?

BACKPLANE STANDART (maintained by PICMG)

- 2 System Slots
- Up to 7 Peripheral Slots
- Full mesh (or Dual star) for *SpFi*, *ETH*, *TSN*, ...
- Dual Star architecture for *SpaceWire*
- *CAN bus (nom. + red.)*
- *I2C* for board monitoring
- Clock signals
- Control signals for individual slot monitoring and control
- Power (nom.+red.): +12V, +5V, (+28V)



Some History ...



Modularity and open standards in Software

• Aeronautics

– Integrated Modular Architecture (IMA)

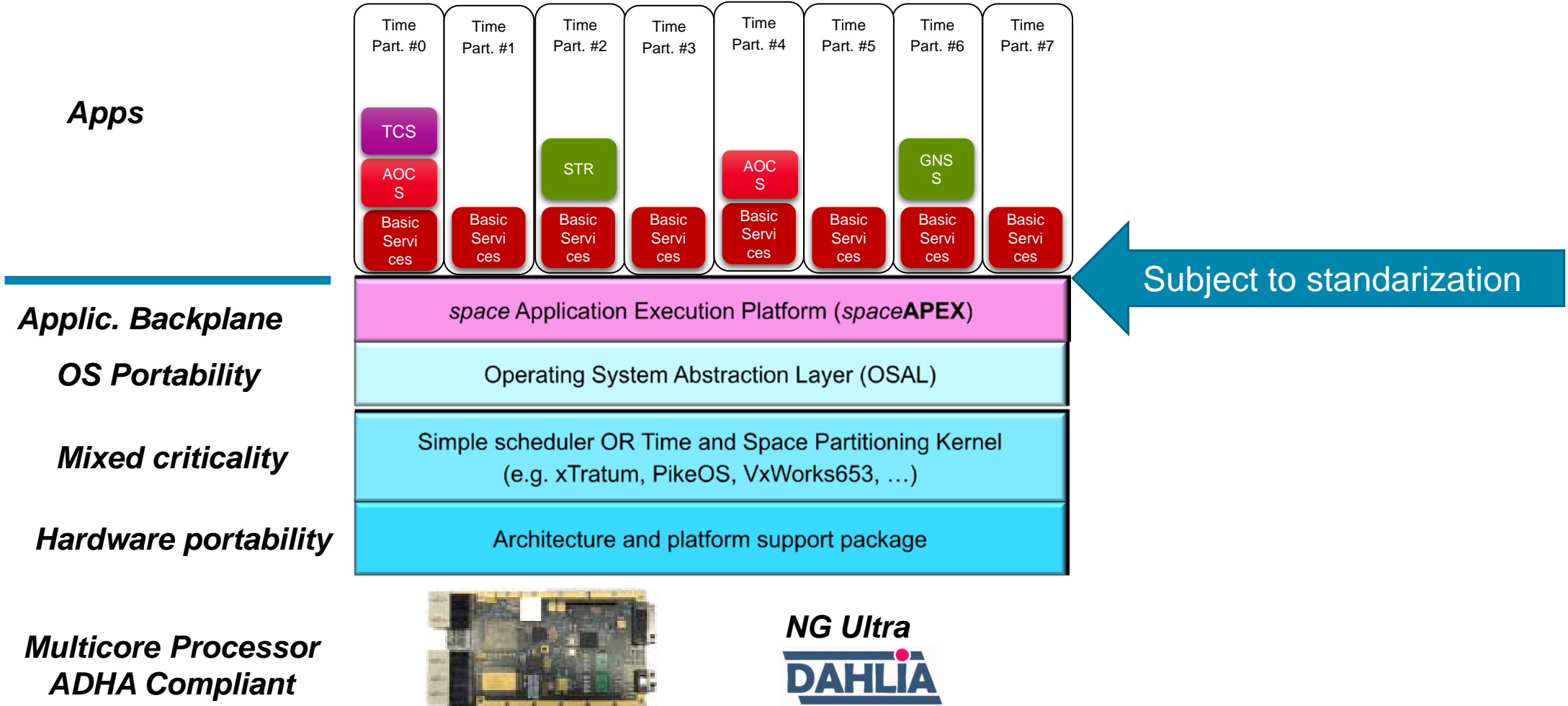
- **Reduced Size, Weight and Power:** Resources are shared between multiple applications instead of having their separate but duplicated resources, costs are lower.
- **Competitiveness:** IMA separated applications from the hardware. Before IMA, vendors supplied both hardware and software together. With IMA, hardware can be bought alone and software can be developed according to the requirements.
- **Portability and Reuse:** Standardized modules can be reused thanks to flexibility provided by IMA.
- **Easier Modification:** Since applications are moved from separate modules to central shared processing unit, it is easier to modify the application compared to asking modifications from the vendors.

... but requirements on processing power are higher → Multicore !!!

Modularity and open standards in Software

- **ARINC (Aeronautical Radio Incorporated) Industry Activities** develops open standards for avionics
 - ARINC 653 is a software specification for space and time partitioning in safety-critical avionics
 - **Application/Executive interface (APEX)** is defined as a set of software interfaces that an ARINC 653 compliant operating system must provide to avionics application developers.
 - **Partition Management Module:** Provides means for modifying the operation mode of partitions.
 - Spatial Partitioning
 - Temporal Partitioning
 - Communication between partitions
 - Queuing ports
 - Sampling ports
 - **Process Management Module:** Each partition can have multiple processes which can be periodic or aperiodic. ARINC 653 partitions are analogous to Windows/Unix processes and ARINC 653 processes are analogous to Windows/Unix threads. Process Management Module provides process scheduling. Each process can be in **Waiting, Ready** or **Running** State.
 - **Time Manager Module:** Time is unique and independent of partition execution. This module provides services like reading time, wait/timeout services for processes, increasing time budget of a process with a hard real time deadline. Together with process management module, they guarantee timely execution of processes in a partition.

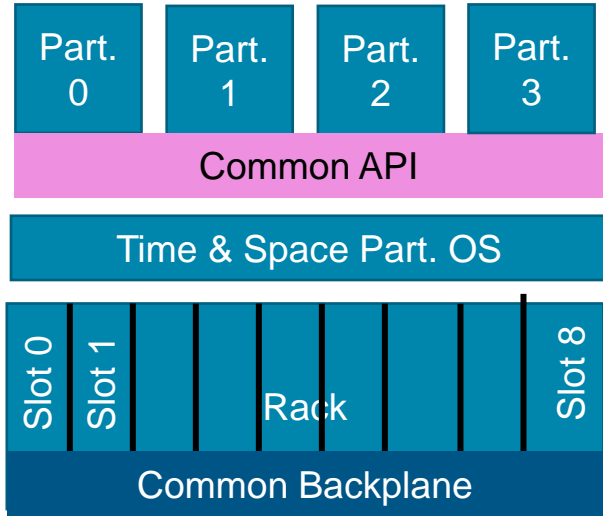
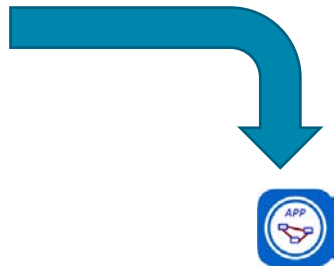
Modularity and Open Standards in Software



GR740 or GR712

App Store

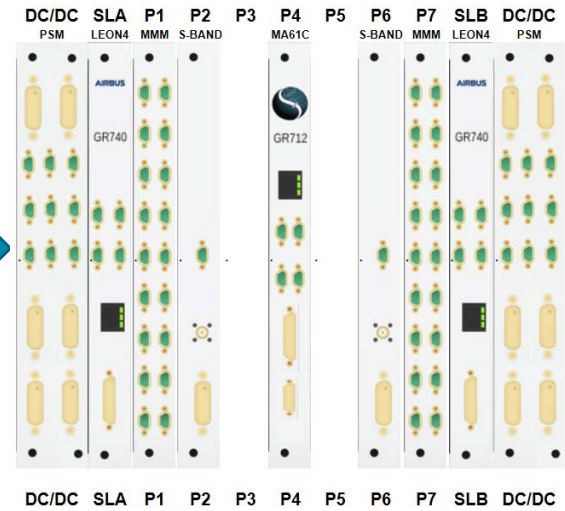
Image Text	Description	Company	Profile	DI	Actions
[13041]	Searchitem	Compar	[6001] ECSS [keine Auswahl]		Go!
[280-100000] RPM [-]	ASI	ECSS TRL 5 1304510000			
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[280-100000] SIF [-]	ASI	ECSS TRL 5 1304510000			
[280-100000] UDP [-]	ASI	ECSS TRL 5 1304510000			
[280-100000] DM [-]	ASI	ECSS TRL 5 1304510000			
[280-100000] OBCEPH [-]	ASI	ECSS TRL 5 1304510000			



Component Store

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[130112]	Searchitem	Compar	[keine Auswahl] [6213] TRL 4		Go!
[205-11134000] Power Conversion Unit 100W [-]	ASP	ProcessProfile TRL 4 1301123000			
[205-1440000] HPPN-3U CPC Serial Executive P400 High performance CPC Board [-]	FOKUS	ECSS TRL 6 1301112400			
[205-420000] HPPN-4-3U CPC Serial Linux high reliability CPC Board [-]	ASB	ECSS TRL 6 1301112400			
[205-510000] ETHSpW Switch [-]	FOKUS	ECSS TRL 6 1301112500			
[205-11132000] 4-Port Switch microdup [-]	ProcessProfile TRL 4 1301125000				
[205-11132000] GNSS Receiver [-]	Jerad	ECSS TRL 4 1301112500			
[205-650000] IO Board [-]	ASI	ProcessProfile TRL 4 1301127000			

“Say goodbye to endless requirement loops and unpredictable pricing. Thanks to open standards, our configurator lets you select the right combination of modules for your test equipment, while giving you real-time feedback on power consumption, mass, volume, price and schedule.”



Test Equipment
<http://www.omac4s.org>



What others say ...



Feb 10th, 2020,
 Ran Qedar,
 CEO,
 SPiN GmbH,
 Darmstadt

Thank you ...

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