

~ Sli.do Q&A ~  
**S-Pwl ASIC Development**

**What is the objective of phase 2?**

The main objective of phase 2 is to design the smps, to assemble the overall design, to manufactured the SPA ASIC and to test it

**Has the distributed RTU a future in the new spacecrafts?**

To reduce the harness as demonstrated by S-Pwl technology, yes definitely

**How is the Powerlink TRL status and roadmap? (Question for TAS and their PwL)**

The activity including the phase 2 were targeting a TRL 5.