

# Demonstration of SpaceFibre Technology Usage for Image Processing Applications (Polish Incentive Scheme)

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TEC-ED & TEC-SW Final Presentation Day

01 June 2021



# Demonstration of SpaceFibre Technology Usage for Image Processing Applications

- Budget: 200 K€
- Duration: 22 months
- Prime: Syderal Polska (+ TAS-S)
- Main Objectives:
  1. To design a System-on-Chip for RTG-4 FPGA based on SpaceFibre & CCSDS Image Compression (SHyLoC) IP cores available through ESA
  2. To verify and validate the correct functionality in simulation and hardware
  3. To benchmark the performance of the processing chain