

SEFUW: Space FPGA Users Workshop, 5th Edition

Tuesday, 14 March 2023

Industrial Experiences - Erasmus High Bay (10:00 - 10:45)

-Conveners: David Merodio Codinachs; David Dangla

time	[id] title	presenter
10:00	[64] FPGA developments in Airbus products	POUPAT, Jean-Luc
10:25	[69] SpaceFibre and SpaceWire IP Cores for the next generation of radiation-tolerant FPGAS (Star-Dundee)	Dr MCCLEMENTS, Chris FARRAS CASAS, Marti

Industrial Experiences - Erasmus High Bay (11:30 - 12:00)

time	[id] title	presenter
11:30	[65] CNES status on Nanoxplore developments	ODOUNDE, AURELIEN MANNI, Florent LAVIGNE, Guillaume

Industrial Experiences - Erasmus High Bay (15:20 - 16:30)

-Conveners: Guillaume Lavigne; Lucana Santos Falcon

time	[id] title	presenter
15:20	[3] Improving Design Quality by Static Code Analysis (Aduvo Engineering Training)	TAYLOR, Adam
15:45	[12] 1Gb to 64Gb High reliability configuration solutions to boot the last generation of FPGAs (3D Plus)	BENARD, Patrice
16:10	[10] High-Performance UVM Verification IP for SpaceWire Codec (IngeniArs & Univ. of Pisa)	VAGAGGINI, Simone

Wednesday, 15 March 2023

Industrial Experiences - Erasmus High Bay (09:30 - 11:10)

-Conveners: David Dangla; David Merodio Codinachs

time	[id] title	presenter
09:30	[56] Lighter than Lite - Lightweight IP Interconnects for Modular FPGA Design and use in the CO2M MAP Instrument (TAS UK)	ROWLINGS, Matthew
09:55	[44] Custom made cameras for deep space navigation and long-range detection (Sodern)	POURRIER, Christophe
10:20	[55] Designing a low-latency Fail-operational motor controller for space on industrial-grade FPGAs (Watt&Well)	WOLFF, Alois
10:45	[54] Design on NG-ULTRA FPGAs using a HLS-to-Bitstream design flow (TAS)	MERCIER, Philippe

Industrial Experiences - Erasmus High Bay (15:25 - 16:45)

-Conveners: David Merodio Codinachs; David Dangla

time	[id] title	presenter
15:25	[17] Evolving the NG-ULTRA SoC and the NG-LARGE SoC software ecosystem (FentlSS)	MASMANO, Miguel
15:50	[7] GRLIB VHDL IP Library (Frontgrade Gaisler)	MALATESTA, Fabio
16:20	[14] FPGAs usage in some science projects at IRAP	RAMBAUD, Damien Mr MAESTRE, Stéphan Mr GUILLERMAND, Jérémy

Thursday, 16 March 2023

Industrial Experiences - Erasmus High Bay (16:05 - 16:55)

-Conveners: David Dangla; Filomena Decuzzi

time	[id] title	presenter
16:05	[60] ARIETIS project, an example of NG-Medium usage (EREMS)	PERRET, jokin
16:30	[73] Accelerated Deep-Learning inference on FPGAs in the Space Domain (ADS)	MENA MORALES, Raphael