



# SEFUW: Space FPGA Users Workshop, 5th Edition

## Wednesday, 15 March 2023

### Poster Session - Erasmus High Bay (11:10 - 11:40)

time	[id] title	presenter
11:10	[52] FPGA- and Flash-based mass memory system: architectures, implementations, industry projects and future trends (DSI-AS / TU Braunschweig)	Dr ZHOU, Gang
11:10	[6] Intel's Solution for Fast and Efficient Inference	ABRA, Ruth
11:10	[16] Payload Control & Data Processing Unit for SmallSats (Technolution B.V.)	Dr RAUWERDA, Gerard
11:10	[40] Estimating the Error Rates of Xilinx Zynq-7000 APSoCs in Space Radiation Environments (University of Piraeus)	Dr AGIAKATSIKAS, Dimitrios
11:10	[21] Practical and economic considerations for improving the reliability of FPGA-based control systems for space applications using a hybrid design approach (VORAGO Technologies)	ODLAND, Keith CUELLO, Germán
11:10	[4] Real Number Gradient Descent Decoding for Fault Tolerant Linear DSP Transforms	AMARICAI, Alexandru

### Poster Session - Erasmus High Bay (16:45 - 17:15)

# Thursday, 16 March 2023

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