

## ***Introduction of SIFSUP***

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The objective of SIFSUP is to define an Harmonized On Board Software Reference Architecture that fuses the OSRA and TSP approaches and that focuses on the interface of the Execution Platform. The study is at the crossroad between the SAVOIR-FAIRE and SAVOIR-IMA working groups.

The challenge of the study originates from:

- 1) the large number of related past and on-going activities (COrDeT's, SISTORA, IMA-SP, PUS and SOIS standards...)
- 2) the difference in paradigm between IMA-SP (whose objective is to define the interfaces of a time-space partitioned system, allowing possibly independent development and validation of partitions) and the On-board Software Reference Architecture (OSRA) (a component-based architecture on top of a centralized Execution Platform that provides the required services).
- 3) the relationship between the technical aspects (the actual architecture) and the industrial processes (taking into account the various actors/roles).

Since SIFSUP is an on-going study, the ADCSS presentation will focus on the outcome of the interviews and on the overall Execution Platform architecture, as defined today.