# SEFUW: SpacE FPGA Users Workshop, 6th Edition

## Wednesday 26 March 2025

**Industrial Experiences** - Newton 1 and 2 (10:10 - 11:00)

| time | [id] title | presenter |
|------|-----------|-----------|
| 10:10 | [40] Space Applications for Versal using Open Standards Based Modules | Mr DOLMAN, David |
| 10:35 | [43] PMBus based space power architecture for high performance FPGAs and Processors | PUCHNER, Helmut |

**Industrial Experiences** - Newton 1 and 2 (16:00 - 17:15)

| time | [id] title | presenter |
|------|-----------|-----------|
| 16:00 | [10] Frontgrade CertusPro-NX-RT FPGA | BARANSKI, Brian |
| 16:25 | [39] A NEW HYPERSPECTRAL COMPRESSION IP CORE FOR SPACE MISSIONS BASED ON CCSDS-123.0-B-2 STANDARD | SANCHEZ, Antonio J. |
| 16:50 | [52] AGGA-4 IP Core for Modern FPGAs | DANEK, Martin |

# Thursday 27 March 2025

**Industrial Experiences - Newton 1 and 2 (11:45 - 13:00)**

| time | [id] title | presenter |
|---|---|---|
| 11:45 | [14] COTS FPGA technology for On-Board Switching | Mr RYBAK, Tomasz |
| 12:10 | [44] KBT Transceiver for Lunar Gateway: FPGA Development and Validation. | ZANABONI, Francesco |
| 12:35 | [50] IngeniaArs IP Cores Portfolio: Satellite Communication, Artificial Intelligence and High-Speed Serial Links | VAGAGGINI, Simone |

**Industrial Experiences - Newton 1 and 2 (14:30 - 16:20)**

| time | [id] title | presenter |
|---|---|---|
| 14:30 | [29] Common Framework for Hardware Root of Trust | Mr JEHKONEN, Petri |
| 14:55 | [24] Evolution of Airbus CRISA products in Space applications: implementation strategies and challenges using Microchip's RTG4 FPGAs | Mrs HIDALGO, Isabel |
| 15:20 | [19] IMPRIO IP Core: FPGA-Based Image Prioritization for Autonomous Operations in ESA's Comet Interceptor Mission | ŠATE, Jānis |
| 15:45 | [37] Flexible Architecture for In-Orbit Radiation Monitoring deployed on the FOBP | BARON, Michael THOME, Moritz |