

SEFUW: Space FPGA Users Workshop, 6th Edition

Thursday 27 March 2025

Design Flow - Einstein (11:45 - 13:00)

time	[id] title	presenter
11:45	[20] A System Design and Rapid Prototyping Methodology Applied to a Fault-Tolerant Vision-Based Navigation Algorithm Targeting MPSoC-FPGA	Dr BOIS, Guy
12:10	[41] Building alternative FPGA toolchains	MILANOVIĆ, Miodrag Dr ENGELHARDT, Nina
12:35	[58] Machine Learning Integration in TASTE for FPGA-Based Space Systems	FERRANDI, Fabrizio