

PRODIGE PROCESSORS FAMILY: FROM 5G/6G TO GNSS & PNT APPLICATIONS

EDHCP-16TH OCTOBER 2025

ALVARO CORTES, CARLOS ABAD, MARGARITA MARTÍNEZ, JUAN-MANUEL RODRÍGUEZ, PHD & RAUL REGADA

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PRODIGE PRODUCT FAMILY

/// PRODIGE: SOFTWARE DEFINED PLATFORM FOR MULTIPURPOSE PROCESSING

Enable the functional or service evolution along the mission lifetime

Modular building blocks allows full flexibility in function definition and implementation Better over time: over-theair-upgrades during the mission for performances / functionalities enhancement



COMPONENTS TECHNOLOGY

Best compromise of components selection for high-performance and in flight reconfiguration with flight heritage



MODULAR AND SCALABLE

Reliable blocks allow full flexibility in functional definition and implementation (I/O frequencies, nb of channels, BW,...)



OVER THE AIR UPGRADES

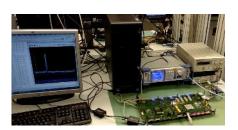
Resilience to market evolution and regulation changes







MULTIPURPOSE SOFTWARE DEFINED PROCESSING PLATFORMS



Al-ready platform since 2014



GEN#3

PRODIGEV3



MAGIN-e (2024)



LADAPv3 (2025)

GEN#4



PRODIGEv4 (exploration)

GEN#1



LADAPv1 (2014)

PRODIGEV1 (2017) PRODIGEV1.5

(2020)

PRODIGEv2

GEN#2

(2021)

GEN#2,5 Low-end

PRODIGE-MINI

(2024)

In flight

In development

In design

R&T

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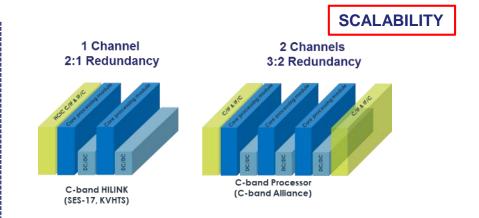


Prodige family share 4 key architectural principles

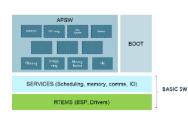
MODULARITY Lego Figure Lego F

C-band HILINK

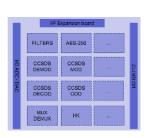
(SES-17, KVHTS)



SW-DEFINED

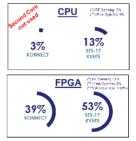


IF-band HILINK (KONNECT)



PROPRIETARY INFORMATION

GROWTH POTENTIAL







PRODIGE v1.X PRODUCT FAMILY HISTORY

v1.0 v1.0 v1.0 v1.5 v1.5 v1.0

Site diversity **SB NEO** Konnect for Africa High speed link SB NEO VHTS Konnect VHTS, SES-17, E10B, SNG...

UHF processor SPAINSAT NG, Eutelsat 36D

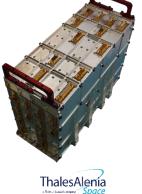
CTEG Amazonas Nexus Inter-satellite link processor Galileo 2G

Data processing unit CHIME

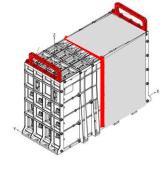
















ThalesAlenia



AIRBUS

hisdeSAT







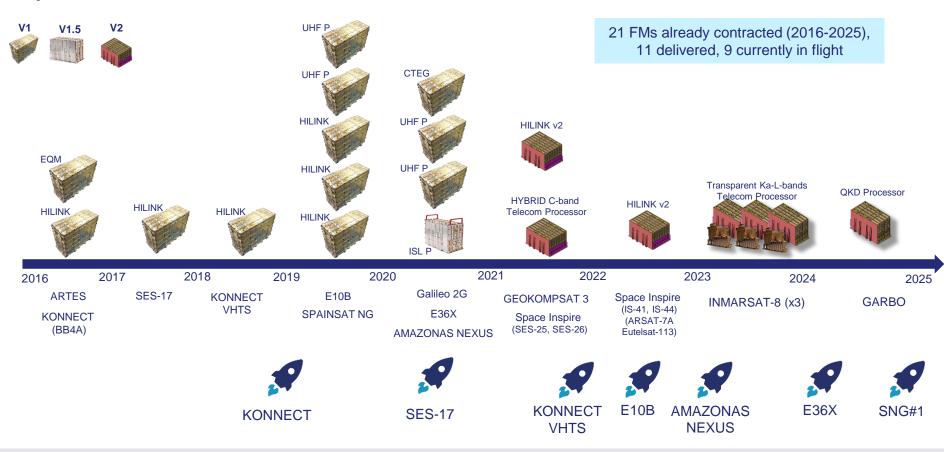




21 FMs already contracted (2016-2022), 7 delivered, 3 currently in flight



PRODIGE CONTRACTS SO FAR



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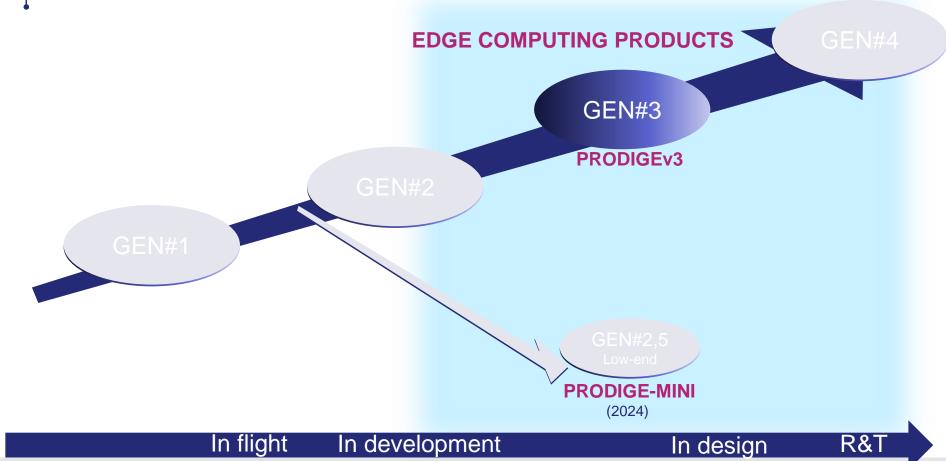
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MULTIPURPOSE SOFTWARE DEFINED PROCESSING PLATFORMS GEN 3



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5G RELEASES & ROUTEMAP



Rel-17 5G NTN

Rel-18 5G-Adv NTN

Rel-19 5G-Adv NTN

Rel-20 6G

/// 5G expected evolutiona along the 3GPP releases

- Release 15 of 3GPP defines for the first time an access network (NR) and a Core Network (5GC) with 5G specifications. In particular, it addresses the requirements derived from the eMBB and uRLLC use case scenarios.
- In Release 16, some improvements are introduced in the previous scenarios, especially in uRLLC. For the time being, mMTC is addressed through the connection of NB-IOT and LTE-MTC technologies to the 5GC.
- Special attention is given to various specific applications that may be important for the deployment and implementation of 5G networks.
- Releases 17 and 18, from 2022 and 2024 respectively, represent the transition to 5G-Advanced, with significant improvements such as the extension of the FR2 range, NTN systems, and RedCap devices.

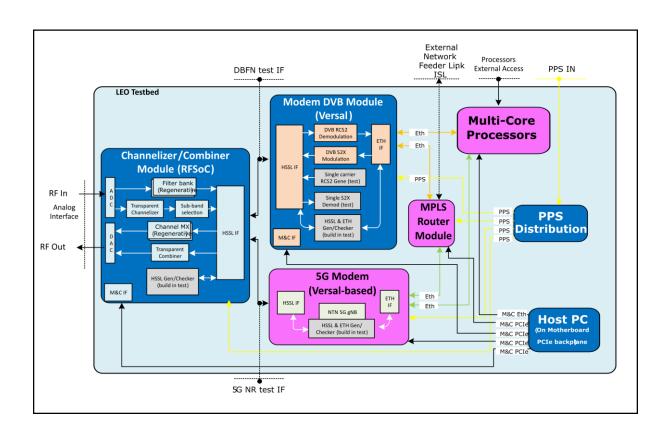


SYSTEM DIAGRAM – LEO REGENERATIVE TESTBED

/// LEO REGENERATIVE TESTBED funtionality diagram.

Breakdown of development into blocks:

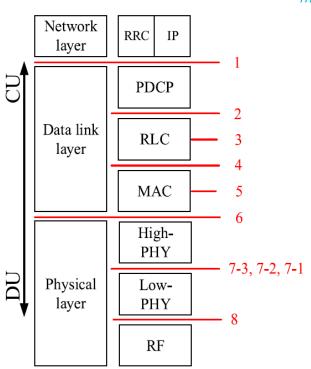
- **Interface units:** HSSL IF, Ethernet IF,
- **Modulation & Processing units:** DVB S2X, DVB RCS2, 5G, Channelizer, Combiner and MPLS Router.
- **Test functions:** RCS2 Generator, S2X Checker, HSSL & Ethernet Generator and Checker

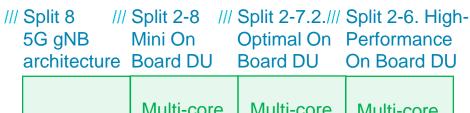




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3. GNB ARCHITECTURE ROUTEMAP



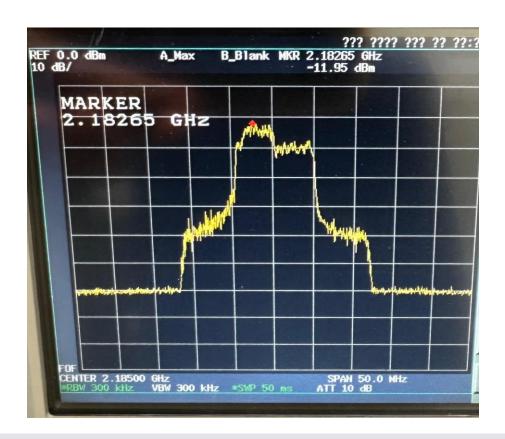


| Multi-core processor | Multi-core processor | Multi-core processor | Multi-core processor |
|-------------------------|-------------------------|-------------------------|-------------------------|
| | Versal PS | Versal PS Versal PL | Versal PS |
| | | | Versal PL |
| Versal PL | Versal PL | VOIGALLE | |
| RFSoC | RFSoC | RFSoC | RFSoC |

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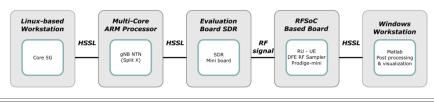




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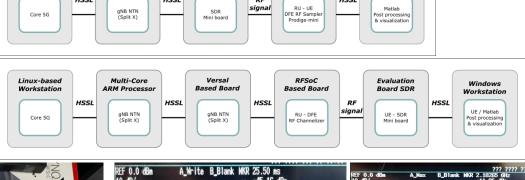
/// Test environment architecture for the 5G RF analysis signals N256 band from gNB

Figure. 1 Preliminary test results



Final test results

Figure. 6





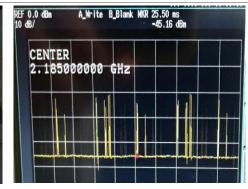




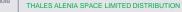
Figure. 4 Figure. 3 Figure. 2

Figure. 5

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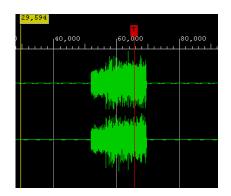
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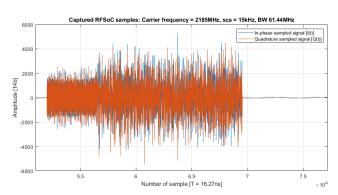




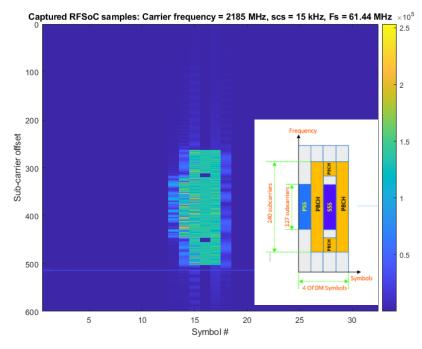
/// Time domain I&Q signals in ILA



/// Time domain I&Q signals extracted to Matlab

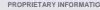


/// 5G RF Analyzer signals N256 band from gNB



Uplink central frequecy: 1995 MHz

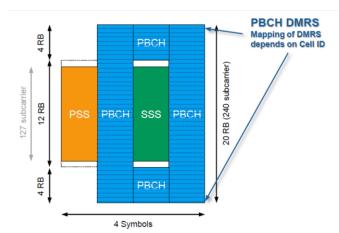
Uplink frequency range: 1980-2010 MHz

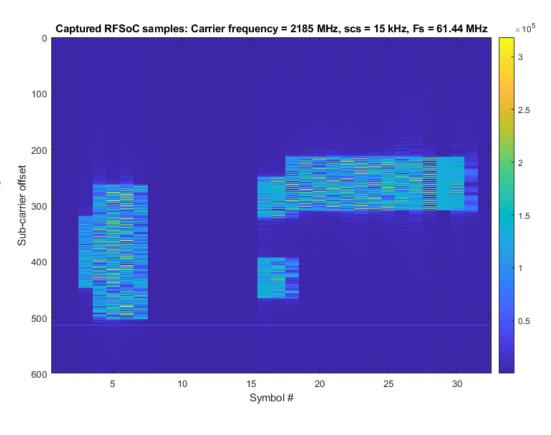


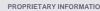


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- /// SSB. Sync Signal Block.
- PSS. Primary Sync Signal.
- SSS. Secondary Sync Signal.
- PBCH. Physical broadcast Channel.
- /// SSB PDCCH & PDSCH tx for SIB1
- PDCCH sends instructions and control info
- PDSCH delivers data content.









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SUMMARY

- Prodige familiy of multi purpouse processors capable tu run High Perform applications regarding the space segment.
 - 2 5G/6G Multiple splits generation Split-8 Split-7.2 Split-6 Split-2 to validate the resources and capabilities and adapt the solution to the final aproach.
 - Analyze the capabilities of the distributed computing along the Multi-Core processors the Versal-based system combined with different ADC/DAC or RFSoC-based resources and capabilities.
- Analyze to get the optimal architecure along the on-board and on-ground distribution.



We believe in Space as humankind's new horizon to build a better, sustainable life on Earth SPACE FOR LIFE



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/// 17

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