Session Program

12-17 Oct 2025

EDHPC 2025 - 2nd European Data Handling & Data Processing Conference

Advanced Processors & SoCs

Thursday 16 October

09:00

Advanced Processors & SoCs
Session | **Convener:** Richard Jansen

09:00–09:20

GR765: A Radiation-Hardened, Fault-Tolerant Octa-Core SoC with Integrated eFPGA for Next-Generation Space Avionics

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09:20–09:40

Microchip's PIC64-HPSC: A Versatile Compute Platform for the Next Era of Spaceflight Systems

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09:40–10:00

Bridging Reliability and High Performance in AI Execution with Radiation-Hardened Co-Processing

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10:00–10:20

On-Board Performance Benchmark ("OBPMark") of Arm NEON SIMD on Teledyne e2v LS1046-Space and LX2160-Space Space-Grade Processors

10:20