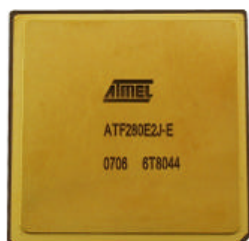


SEFUW: Space FPGA Users Workshop; 2nd Edition



David Merodio Codinachs (ESA)
David Dangla (CNES)



WELCOME TO ESTEC



European Space Research and Technology Centre (ESTEC)

<http://www.esa.int/SPECIALS/ESTEC/index.html>



Coffee breaks and
Cocktail reception during Demo are
sponsored by

THALES Communication and Security

The THALES logo, with the word 'THALES' in a bold, dark blue, sans-serif font. A small teal dot is positioned above the letter 'A'.

Exchange of highly valuable
information and experience
among FPGA users, researchers,
CAD vendors and FPGA vendors

Precursor experiences (I)

<http://spacefpga.atmel-nantes.fr/spacefpga/>

ATMEL Aerospace FPGA
working group:
Workshops and website

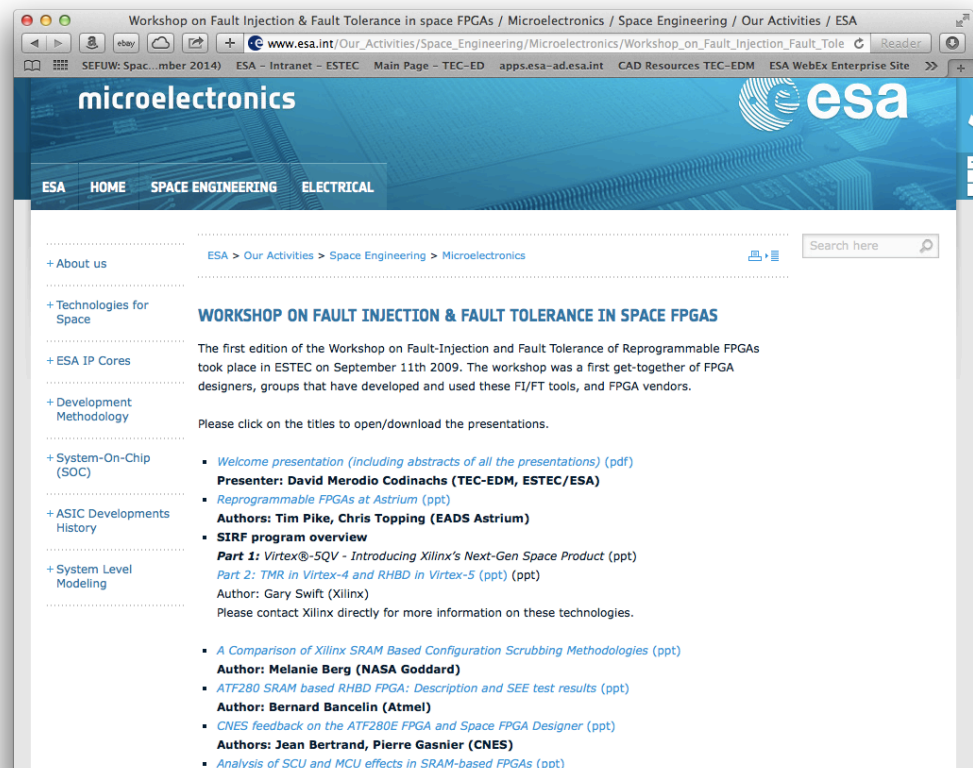


http://www.esa.int/TEC/Microelectronics/SEMV57KIWZF_0.html

Workshop on Fault Injection & Fault Tolerance in space FPGAs (2009)

The first edition of the Workshop on Fault-Injection and Fault Tolerance of Reconfigurable FPGAs took place in ESTEC on September 11th 2009.

The workshop was a first get-together of FPGA designers, groups that have developed and used these FI/FT tools, and FPGA vendors.

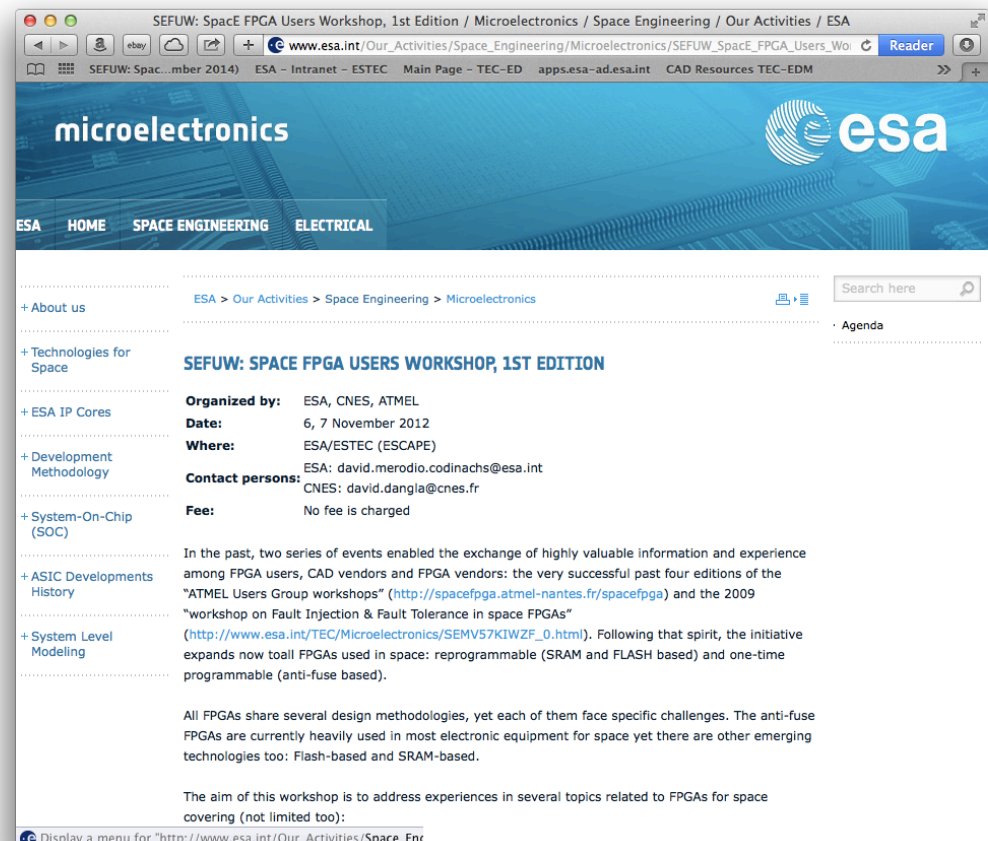


Precursor experiences (III)



http://www.esa.int/Our_Activities/Space_Engineering/Microelectronics/SEFUW_SpacE_FPGA_Users_Workshop_1st_Edition

First edition of the SpacE FPGA Users Workshop (2012)



Agenda (I)



<http://space-env.esa.int/indico/conferenceTimeTable.py?confId=59#all.detailed>

The agenda has been distributed among the participants

SEFUW: Space FPGA Users Workshop, 2nd Edition (16-18 September 2014)

space-env.esa.int/indico/conferenceTimeTable.py?confId=59#all.detailed

SEFUW: Space FPGA Users Workshop, 2nd Edition

16-18 September 2014
European Space Research and Technology Centre (ESTEC)
Europe/Amsterdam timezone

Note: once in "Programme" please click on "detailed view" in order to see the detailed agenda

SEFUW 2014

- Call for Abstracts
 - View my abstracts
 - Submit a new abstract
- Programme
- My conference
 - My contributions
- Registration
 - Modify my registration
- Accommodation
- Venue and Practical Information
- Past and Related Events
- SEFUW Dinner

For information please write to
sefuw@esa.int

Tue 16/09 Wed 17/09 Thu 18/09 All days

Print PDF Full screen Detailed view Filter

Session legend

Demo Session and Coc... Design for Space Applic... Design for Space Applic... FPGA/CAD Vendors

Tue 16/9

08:00

Registration and Early Morning Networking Break sponsored by Thales Communications & Security

Newton 2, European Space Research and Technology Centre (ESTEC) 09:30 - 10:00

10:00 Welcome Mr. David DANGLA et al.
Newton 2, European Space Research and Technology Centre (ESTEC) 10:00 - 10:15

RHBD FPGA AT40K Family Ms. Valerie BRIOT

11:00 Newton 2, European Space Research and Technology Centre (ESTEC) 10:15 - 11:05
IDS/FIGARO Enhancement Program Mr. Yohann BRICARD

1. Sessions:

- a. FPGA/CAD Vendors
- b. Design for space applications: tools
- c. Design for Space Applications: Fault Tolerance Methodologies
- d. Industry experiences
- e. Radiation Testing

2. Demo session

1. Presenters:

- a. Provide the presentations (preferably during the breaks) or have your laptop ready
- b. Do you agree on having it available at the ESA website? **Default: YES** (the final version can be provided another day)

2. Attendees:

- a. Do you agree to distribute your email in the attendees list? **Default: YES**

1. Networking Coffee Break sponsored by Thales Communications & Security

2. Networking Luncheon at ESTEC main canteen

3. Wireless access:

- a. Login and password included at the back of your visitor badge. Valid throughout the workshop

SSID: esa-public Authentication: Open
Username: d.merodiocodinachs
Password: xxxxxx

Restaurant: Iets Anders in Bistro Bardot

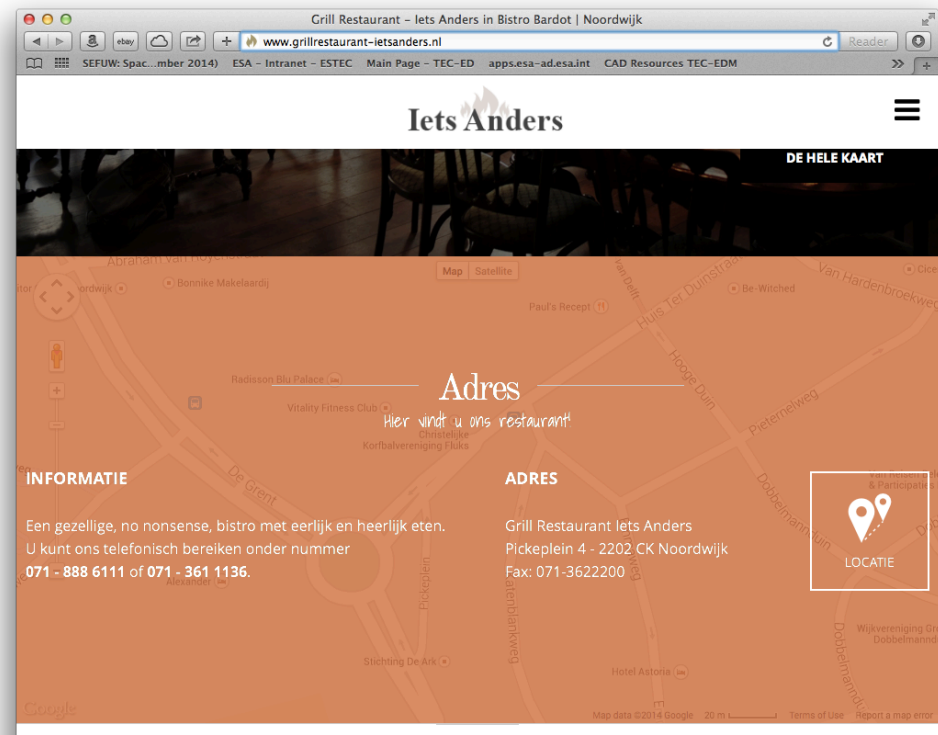
Date: 16th September

At 8pm

Address: Pickeplein 4,
2202 CK Noordwijk

Price: 42,5 EUR

The price includes a 3
course menu and drink
arrangement.



Confirmation: Before lunch time (if not done yet)

ENJOY THE WORKSHOP !!!