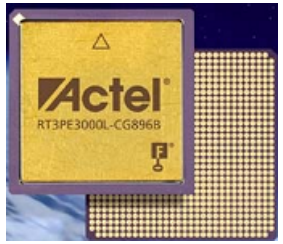
SEFUW: SpacE FPGA Users Workshop; 2nd Edition



David Merodio Codinachs (ESA)
David Dangla (CNES)

Location: thanks to the facility

European Space Research and Technology Centre (ESTEC)

http://www.esa.int/SPECIALS/ESTEC/index.html

Thanks to the Sponsor

Coffee breaks and

Cocktail reception during Demo are sponsored by

THALES Communication and Security

THALES

SEFUW Objective

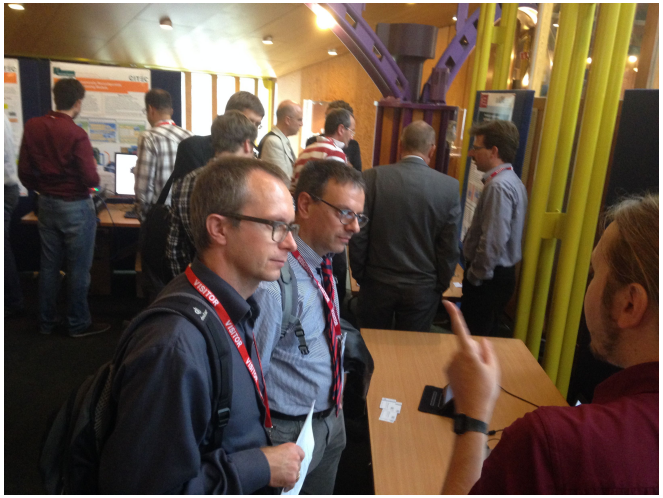
Exchange of highly valuable information and experience among FPGA users, researchers, CAD vendors and FPGA vendors

Covered Agenda

1. **Sessions**:
    a. FPGA/CAD Vendors
    b. Design for space applications: tools
    c. Design for Space Applications: Fault Tolerance Methodologies
    d. Industry experiences
    e. Radiation Testing
2. **Demo** session

And many discussions during breaks:
COTS FPGAs usage, mitigation techniques, packaging, ITAR status, etc …

DEMO, some pictures



Organization notes

1. **Presenters**:
    a. Thank you for presenting your work in the SpacE FPGA Users Workshop
    b. All presentations will be available online
    c. Do you agree on having it available at the ESA website? **Default: YES** (the final version can be provided another day)
2. **Attendees**:
    a. Thank you for your active participation
    b. Do you agree to distribute your email in the attendees list? **Default: YES**

Attendees

TOTAL	101
ESA	17
EXTERNAL	84

21 Nationalities

Optional social dinner

Restaurant: Iets Anders in Bistro Bardot

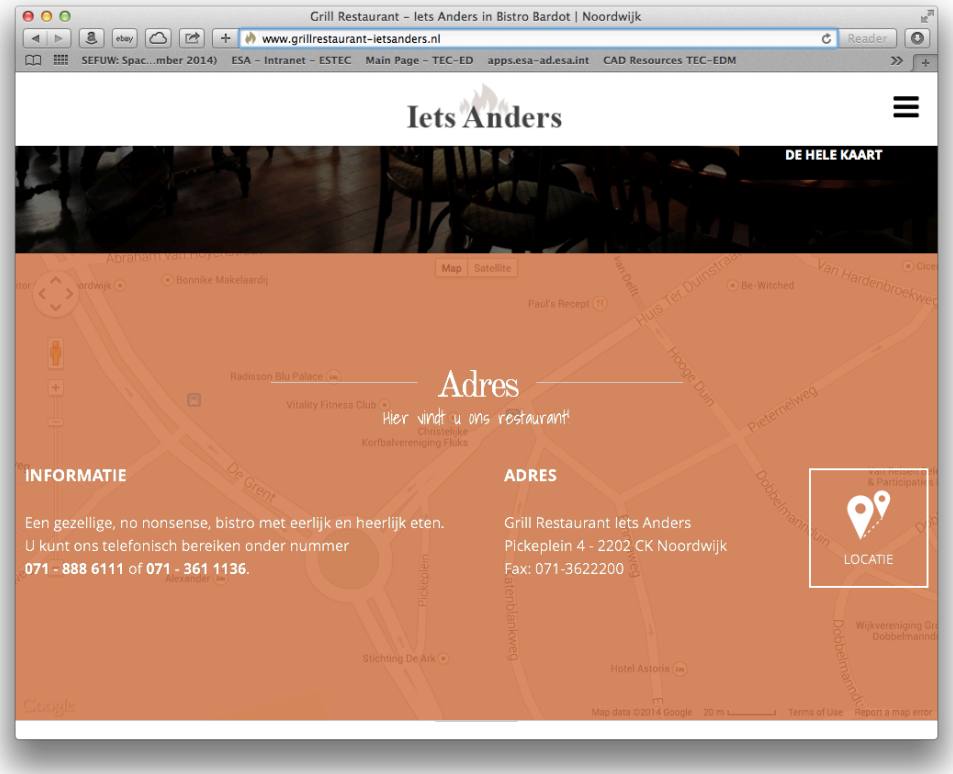
Date:16th September

At 8pm

Address: Pickeplein 4, 2202 CK Noordwijk

Price: 42,5 EUR

The price includes a 3 course menu and drink arrangement.



Do you need a receipt? Please let me know

See you next at the next edition of SEFUW

THANK YOU ALL !

Have a safe return home