

# **DEEP SUBMICRON ASIC PLATFORM FOR SPACE – PERSPECTIVES FOR USE IN HIGH PERFORMANCE DIGITAL PROCESSING SYSTEMS**

## **Session: ASIC Processes**

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## **ABSTRACT**

The purpose of the presentation is to give perspectives around ASIC technologies for space. The concept of deep submicron ASIC platform for space will be presented, as well as its potential use for digital signal processing applications, including High Speed serial links and other IPs for system-on-chip designs. Packaging, radiation, reliability aspects will also be addressed.