



# ESA Workshop on Avionics, Data, Control and Software Systems - ADCSS 2007

## Friday, 5 October 2007

**Next Generation Processor for On-Board Payload Data Processing Applications - Newton Conference Center (09:00 - 16:00)**

time	[id] title	presenter
09:00	[80] RadSafe ASSP	GINOSAR, R.
09:25	[81] Space Fibre	PARKES, S.
09:50	[82] Deep Submicron AIC Platform Approach for Next Satellite Telecom Processors - Possible Application to Other Processors	RENAUD, N.
10:15	[83] High Perf. ASIC Platform Approach for Next Satellite Telecom Processors - Possible Application to Other Processors	DUGOUJON, ET AL., L.
10:40	[84] Round Table Discussion	Mr SUESS, Martin
10:55	Coffee Break	
11:10	[85] Dynamically Reconfigurable Processor for Space Application	SCHUELER, E.
11:35	[86] A Montium Based Dynamically Reconfigurable Multi-Core for High Performance DSP Applications	HEYSTERS, ET AL, PM
12:00	[87] A Dynamically Reconfigurable Processor Architecture for Future Space Missions' Payload Data Processing	ARSLAN, ET AL., T
12:25	[88] A High-performance, Low-power, Flexible Platform for Next generation On-Board Payload Data Processing Application	BEESE, T
12:50	[89] Payload Processing Based on LEON3FT SMP Architecture	GAISLER, J.
13:15	Lunch Break	
14:15	[90] Roadmap for NG Processor for Payload Applications - Discussion and Conclusion	Mr SUESS, Martin Mr ARMBRUSTER, Philippe