



# ESA Workshop on Avionics, Data, Control and Software Systems - ADCSS 2007

## Thursday, 4 October 2007

**Next Generation Processor for On-Board Payload Data Processing Applications - Newton Conference Center (09:15 - 18:10)**

time	[id] title	presenter
09:15	[62] Introduction	Mr SUESS, Martin
09:20	[63] Overview of Future Earth Observations Mission	BENSI, P.
09:45	[64] On-Board Payload Data Processing Requirements for Future CNES Missions	REMETEAN, ET AL., E.
10:10	[65] Evaluations of Next Generation DSP Solutions	NOTEBAERT, ET AL., O.
10:35	Coffee Break	
10:50	[66] Overview by Thales Alenia Space fo High Power Processing Needs for Future Programs	LECONTE, ET AL., P.
11:15	[67] TSC21020 Application Experience Aiding Next Generation Spaceborne DSP Selection	SUST, M.
11:40	[68] Review of Possible Implementation Solution	EPOSTI, ET AL., ML
12:05	[69] Round Table Discussion	Mr SUESS, Martin
13:00	Lunch Break	
13:35	[70] HPDP - High Performance Data Processor	SYED, M.
14:00	[71] The AT697E LEON2-FT Device Integration in IPPM (Integrated Payload Data Processing Module)	WALER, ET AL., E
14:25	[72] MDPA - Multi-DSP/micro Processor Architecture	HELTERS, ET AL., T.
14:50	[73] Leon Co-Processor for Payload Data Processing	HONVAULT, C.
15:15	[74] Round Table Discussion	Mr SUESS, Martin
15:45	Coffee Break	
16:00	[75] An Example of High Processing Power Payload Computer: the GAIA Video Processing Unit	SOUCAILLE, ET AL., JF
16:25	[76] Use of Texas Instruments' DSP in Space Programs	MAGISTRATI, G.
16:50	[77] TI Roadmap for Space DSP	WILLIGER, I.
17:15	[78] Using Low Power COTS DSP: Assessment of Analog Devices Blackfin	REMETEAN, ET AL., E.
17:40	[79] Round Table Discussion	Mr SUESS, Martin