3rd ESA Workshop on Avionics, Data, Control and Software Systems - ADCSS 2009

Wednesday, 4 November 2009

Micro-Processors for Space Applications - Newton Conference Center (09:00 - 18:00)

time	[id] title	presenter
09:00	[11] Welcome	
09:10	[12] Introduction	WEIGAND, R.
09:25	[13] TSC695F, AT697E/F	
09:45	[14] AT7913 SpW-RTC	Mr ILSTAD, Jørgen
10:00	[15] Next Generation Micro Processor	
10:20	[17] SCOC3 Asic	
10:40	[16] Software Development Tools synthesis	Mr HJORTNAES, Kjeld
11:00	Coffee Break	
11:15	[18] Introduction	FERNANDEZ-LEON, A.
11:30	[19] The RUAG experience and development strategy	
11:50	[20] The Astrium experience and development strategy	
12:10	[21] The TAS experience and development strategy	
12:30	[22] The Syderal experience and development strategy	
12:45	[23] The Verhaert experience and development strategy	
13:00	Lunch Break	
14:00	[24] HW/SW & New related technology developments	Mr MAGISTRATI, Giorgio Mr HJORTNAES, Kjeld
14:40	[25] Next generation DSP roadmap and related ESA activities	Dr TRAUTNER, Roland
15:00	[26] SHARC(TM) Digital Signal Processor Core Technology and the Flexibility to Use for Custom Integration	
15:15	[27] Possibilities for NGDSP implementation based on ATMEL DSP IP	
15:30	[28] Texas Instruments COTS DSP chips for space applications	
15:45	[29] High Integration Digital Control Module - Control Loop Processor	
16:05	Lunch Break	
16:20	[30] Use of Cots Processors in space	Mr MONTELEONE, Claudio
16:40	[31] Round Table & Summary of the Workshop	
17:30	[32] Closure of Round Table	