

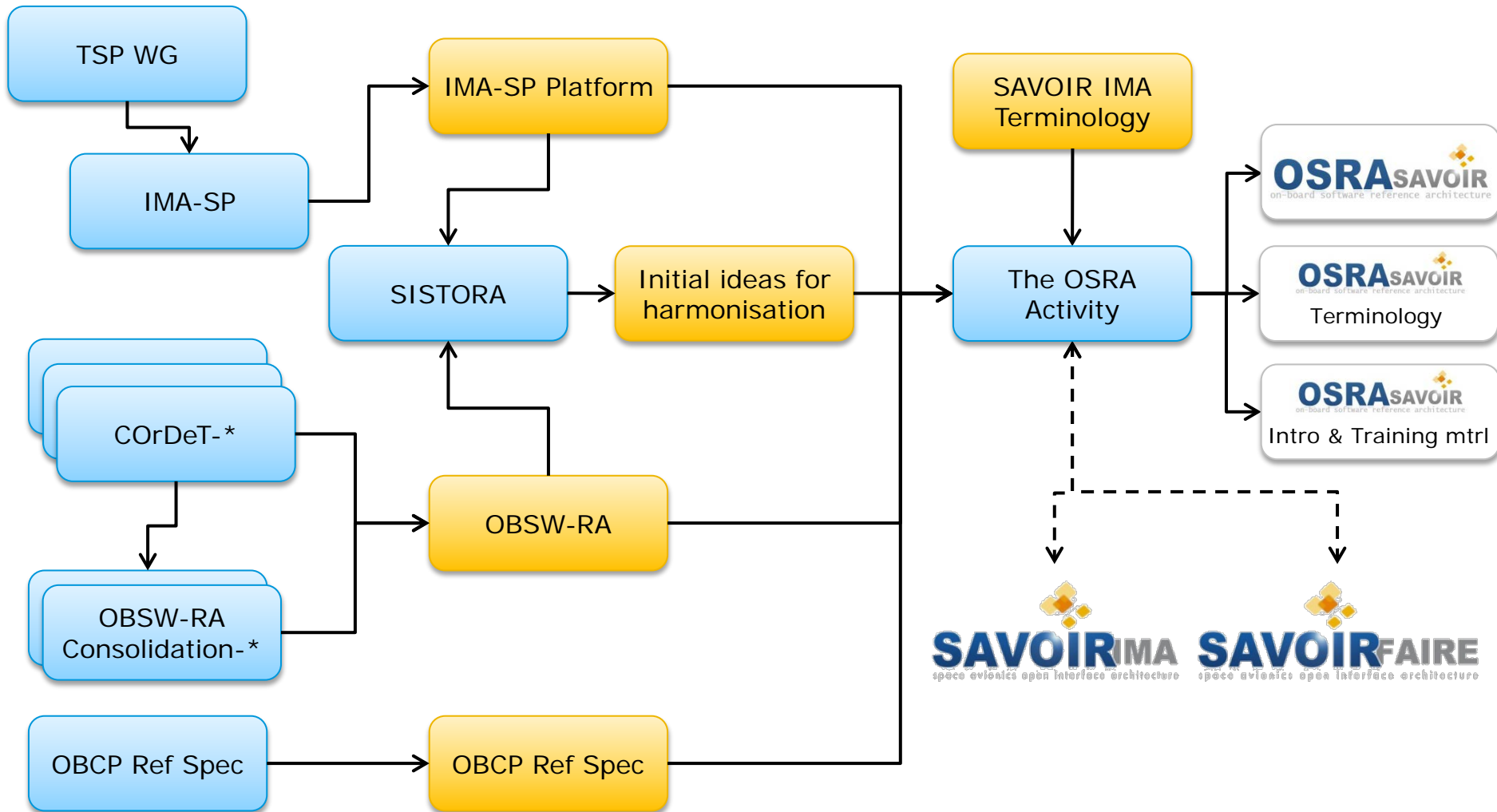
# SAVOIR-FAIRE and SAVOIR-IMA harmonisation strategy

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# We are coming to a junction



- Objective
  - Produce a harmonised on-board software reference architecture suitable for
    - Non-IMA as well as IMA systems
    - Single-processor as well as multi-processor systems
    - Possibly, single-core as well as multi-core systems
- Input
  - Output from COrDeT, IMA-SP, SISTORA, OBCP Ref Spec
    - Results
    - Open issues
    - Future work
  - Other SAVOIR-FAIRE and SAVOIR-IMA selected results
- Output
  - SAVOIR OSRA (architecture description, interfaces, component model spec, etc.)
  - SAVOIR OSRA Terminology
  - Introduction and training material
- Time plan
  - Start Q1 2013
  - Duration 9 months
- Budget
  - 350 k€

- The OSRA will fall under both SAVOIR FAIRE and SAVOIR IMA
  - Both working groups will follow the activity and review the results
- SAVOIR FAIRE will focus more on
  - Overall software architecture
  - Application level considerations
  - Modelling and tooling aspects
- SAVOIR IMA focus more on
  - IMA considerations
  - Partition level considerations (e.g. reuse)
- Both need to focus equally on
  - Services and interfaces
  - Schedulability considerations
  - Execution Platform

- SAVOIR FAIRE will mainly follow and address issues around the OSRA
- SAVOIR IMA will also be considering
  - IMA on current hardware
  - Future hardware enhancements for IMA support
  - Qualification issues for separation kernels

- The OSRA brings together SAVOIR FAIRE and SAVOIR IMA
  - One reference architecture
  - One terminology
- SAVOIR FAIRE and SAVOIR IMA work in parallel
  - With tight couplings

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## Contact SAVOIR-FAIRE

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