

Session Program

24-25 Nov 2026

VISSA: VISible detection for Space Applications

Architecture & Building Blocks

ESA-ESTEC, ERASMUS High-bay
Keplerlaan 1, Noordwijk, 2201 AZ

Tuesday 24 November

13:50

Architecture & Building Blocks

Session | **Location:** ESA-ESTEC, ERASMUS High-bay, Keplerlaan 1, Noordwijk, 2201 AZ | **Convener:** Aurélien Vriet

13:50–14:10

An Architecture and Processes for Construction of 3D image Sensor

Speaker

Robert Hannebauer

14:10–14:30

Comparator With Reduced Input-Common-Mode-Dependent Offset for Low-FPN CMOS Image Sensor Column Readout

Speaker

Pengfei Xu

14:30–14:50

Tileable IA-SAR ADC for 3D-Stacked Image Sensors

Speaker

Hans Berge

14:50–15:10

Comparison between MOS Pillars and Pixel Binning strategies for Native High Dynamic Range: Case Study of a 3 μ m-Pitch Pixel

Speaker

Cyprien Coulon

15:10–15:30

PicoAD: An APD-engineered starting wafer for low-light and time-resolved CMOS Image Sensors

Speaker

Thanushan Kugathasan

15:30